# The RF Line

# NPN Silicon High-Frequency Transistor

Designed primarily for use in high–gain, low–noise small–signal amplifiers for operation up to 2.5 GHz. Also usable in applications requiring fast switching times.

- High Current–Gain Bandwidth Product
- Low Noise Figure @ f = 1.0 GHz NF(matched) = 1.9 dB (Typ)
- High Power Gain —

 $G_{pe(matched)} = 12.0 \text{ dB (Typ)} @ f = 1.0 \text{ GHz}$ 

- Surface Mounted SOT–23 Offers Improved RF Performance, Lower Package Parasitics and High Gain
- Available in tape and reel packaging options:

T1 suffix = 3,000 units per reel T3 suffix = 10,000 units per reel

> NOT RECOMMENDED FOR NEW DESIGNS; PRODUCT TO BE PHASED OUT.

# **MMBR901LT1, T3**

IC = 30 mA
SURFACE MOUNTED
HIGH-FREQUENCY
TRANSISTOR
NPN SILICON



CASE 318-08, STYLE 6 SOT-23

### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	VCEO	15	Vdc
Collector–Base Voltage	V <sub>СВО</sub>	25	Vdc
Emitter–Base Voltage	V <sub>EBO</sub>	2.0	Vdc
Collector Current — Continuous	IC	30	mAdc
Power Dissipation @ $T_C = 75^{\circ}C$ (1) Derate above $75^{\circ}C$	P <sub>D(max)</sub>	0.300 4.0	Watt mW/°C
Storage Temperature Range	T <sub>stg</sub>	-55 to +150	°C
Maximum Junction Temperature	T <sub>J(max)</sub>	150	°C

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Storage Temperature	T <sub>stg</sub>	150	°C
Thermal Resistance, Junction to Case	$R_{\theta JC}$	250	°C/W

### **DEVICE MARKING**

MMBR901LT1, T3 = 7A

### NOTE:

1. Case temperature measured on collector lead immediately adjacent to body of package.

MOTOROLA





# Freescale Semiconductor, Inc.

**ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Collector–Emitter Breakdown Voltage (I <sub>C</sub> = 1.0 mAdc, I <sub>B</sub> = 0)	V(BR)CEO	15	_	_	Vdc
Collector–Base Breakdown Voltage (I <sub>C</sub> = 0.1 mAdc, I <sub>E</sub> = 0)	V(BR)CBO	25	_	_	Vdc
Emitter–Base Breakdown Voltage (I <sub>E</sub> = 0.1 mAdc, I <sub>C</sub> = 0)	V(BR)EBO	2.0	_	_	Vdc
Collector Cutoff Current (V <sub>CB</sub> = 15 Vdc, I <sub>E</sub> = 0)	I <sub>CBO</sub>	_	_	50	nAdc
ON CHARACTERISTICS					
DC Current Gain (IC = 5.0 mAdc, VCE = 5.0 Vdc)	hFE	50	_	200	_
UNCTIONAL TESTS	•				
Minimum Noise Figure (V <sub>CE</sub> = 6.0 Vdc, I <sub>C</sub> = 5.0 mA, f = 1.0 GHz) (V <sub>CE</sub> = 10 Vdc, I <sub>C</sub> = 5.0 mA, f = 1.0 GHz)	NF <sub>min</sub>	_	1.9	_	dB
SMALL-SIGNAL CHARACTERISTICS	•				
Output Capacitance (V <sub>CB</sub> = 10 Vdc, I <sub>C</sub> = 5.0 mAdc, f = 1.0 GHz)	C <sub>obo</sub>	_	_	1.0	pF
Common–Emitter Amplifier Gain (VCC = 6.0 Vdc, IC = 5.0 mAdc, f = 1.0 GHz)	G <sub>pe</sub>	_	12	_	dB



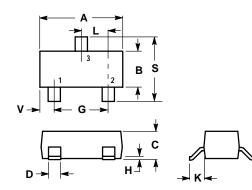


## **NOTES**



### Freescale Semiconductor, Inc.

### PACKAGE DIMENSIONS



#### NOTES

- DIMENSIONING AND TOLERANCING PER ANSI
  Y14 5M 1982
- 2. CONTROLLING DIMENSION: INCH.
- 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.

	INCHES MILLIN			IETERS	
DIM	MIN	MAX	MIN	MAX	
Α	0.1102	0.1197	2.80	3.04	
В	0.0472	0.0551	1.20	1.40	
С	0.0350	0.0440	0.89	1.11	
D	0.0150	0.0200	0.37	0.50	
G	0.0701	0.0807	1.78	2.04	
Н	0.0005	0.0040	0.013	0.100	
J	0.0034	0.0070	0.085	0.177	
K	0.0140	0.0285	0.35	0.69	
L	0.0350	0.0401	0.89	1.02	
S	0.0830	0.1039	2.10	2.64	
V	0.0177	0.0236	0.45	0.60	

STYLE

PIN 1. BASE

2. EMITTER

3. COLLECTOR

CASE 318-08 ISSUE AF

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#### How to reach us:

**USA/EUROPE/Locations Not Listed**: Motorola Literature Distribution; P.O. Box 5405, Denver, Colorado 80217. 1–303–675–2140 or 1–800–441–2447

JAPAN: Motorola Japan Ltd.; SPD, Strategic Planning Office, 141, 4–32–1 Nishi–Gotanda, Shinagawa–ku, Tokyo, Japan. 81–3–5487–8488

Customer Focus Center: 1-800-521-6274

Mfax™: RMFAX0@email.sps.mot.com - TOUCHTONE 1-602-244-6609 Motorola Fax Back System - US & Canada ONLY 1-800-774-18

TOUCHTONE 1–602–244–6609
 US & Canada ONLY 1–800–774–1848
 ASIA/PACIFIC: Motorola Semiconductors H.K. Ltd.; 8B Tai Ping Industrial Park, 51 Ting Kok Road, Tai Po, N.T., Hong Kong. 852–26629298

- http://sps.motorola.com/mfax/

