

# Freescale Semiconductor Advance Information

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# MPC755 RISC Microprocessor Hardware Specifications Addendum for the XPC755xxxnnnLE Series

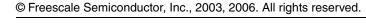
This document describes part-number-specific changes to recommended operating conditions and revised electrical specifications, as applicable, from those described in the general *MPC755 RISC Microprocessor Hardware*Specifications (MPC755EC). The MPC755 and MPC745 are reduced instruction set computing (RISC) microprocessors that implement the PowerPC<sup>TM</sup> instruction set architecture. The devices described in this specification are no longer in production and this document is provided for reference only. For recommended upgrades or replacement devices, contact your Freescale sales office.

Specifications provided in this document supersede those in the *MPC755 RISC Microprocessor Hardware*Specifications, Rev. 6 or later, for the part numbers listed in Table A only. Specifications not addressed herein are unchanged.

Note that headings and table numbers in this document are not consecutively numbered. They are intended to correspond to the heading or table affected in the general hardware specification. Freescale Part Numbers Affected:

XPC755BRX400LE XPC755BPX400LE XPC755CRX450LE

This document contains information on a new product. Specifications and information herein are subject to change without notice.







Part numbers addressed in this document are listed in Table A. For more detailed ordering information, see Section 10, "Ordering Information."

Table A. Part Numbers Addressed by This Data Sheet

	Ор	erating Condition	ıs			
Freescale Part Number	CPU Frequency (MHz)	V <sub>DD</sub>	T <sub>J</sub> (°C)	Significant Differences from Hardware Specification		
XPC755BRX400LE	400	2.0 V ±100 mV	0 to 105	Modified power specifications. These devices		
XPC755BPX400LE				are no longer in production.		
XPC755CRX450LE	450					

**Note:** The X prefix in a Freescale PowerPC part number designates a "Pilot Production Prototype" as defined by Freescale SOP 3-13. These are from a limited production volume of prototypes manufactured, tested, and Q.A. inspected on a qualified technology to simulate normal production. These parts have only preliminary reliability and characterization data. Before pilot production prototypes may be shipped, written authorization from the customer must be on file in the applicable sales office acknowledging the qualification status and the fact that product changes may still occur while shipping pilot production prototypes.

### 4.1 DC Electrical Characteristics

Table 3. Recommended Operating Conditions <sup>1</sup>

		Recomme	nded Value	Unit	Notes
Characteristic	Symbol	400 MHz,	450 MHz		
		Min	Max		
Core supply voltage	$V_{DD}$	1.90	2.10	V	2
PLL supply voltage	$AV_DD$	1.90	2.10	V	2
L2 DLL supply voltage	L2AV <sub>DD</sub>	1.90	2.10	V	2

#### **Notes**

- 1. These are the recommended and tested operating conditions. Proper device operation outside of these conditions is not quaranteed.
- 2. 2.0 V nominal.



**Table 7. Power Consumption for MPC755** 

	Processor (CF	l locit	Natas				
	400 MHz	450 MHz	Unit	Notes			
Full-Power M	ode						
Typical	4.0	4.6	W	1, 3, 4			
Maximum	6.0	8.0	W	1, 2			
Doze Mode							
Maximum	2.3	2.8	W	1, 2, 4			
Nap Mode							
Maximum	1.0	1.0	W	1, 2, 4			
Sleep Mode							
Maximum	470	470	mW	1, 2, 4			
Sleep Mode (PLL and DLL Disabled)							
Maximum	430	430	mW	1, 2			

#### Notes:

- These values apply for all valid processor bus and L2 bus ratios. The values do not include I/O supply power (OV<sub>DD</sub> and L2OV<sub>DD</sub>) or PLL/DLL supply power (AV<sub>DD</sub> and L2AV<sub>DD</sub>). OV<sub>DD</sub> and L2OV<sub>DD</sub> power is system dependent, but is typically <10% of V<sub>DD</sub> power. Worst case power consumption for AV<sub>DD</sub> = 15 mW and L2AV<sub>DD</sub> = 15 mW.
- 2. Maximum power is measured at nominal V<sub>DD</sub> (see Table 3) while running an entirely cache-resident, contrived sequence of instructions which keep the execution units maximally busy.
- 3. Typical power is an average value measured at the nominal recommended  $V_{DD}$  (see Table 3) and 65°C in a system while running a typical code sequence.
- 4. Not 100% tested. Characterized and periodically sampled.

### 4.2.1 Clock AC Specifications

#### **Table 8. Clock AC Timing Specifications**

At recommended operating conditions (see Table 3)

		Maximum Processor Core Frequency					
Characteristic	Symbol	400 MHz		450 MHz		Unit	Notes
		Min	Max	Min	Max		
Processor frequency	f <sub>core</sub>	200	400	200	450	MHz	1
VCO frequency	f <sub>VCO</sub>	400	800	400	900	MHz	1

#### Note:

Caution: The SYSCLK frequency and PLL\_CFG[0:3] settings must be chosen such that the resulting SYSCLK (bus)
frequency, CPU (core) frequency, and PLL (VCO) frequency do not exceed their respective maximum or minimum operating
frequencies. Refer to the PLL\_CFG[0:3] signal description in Section 1.8.1, "PLL Configuration," for valid PLL\_CFG[0:3]
settings.

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**Ordering Information** 

# 10 Ordering Information

## 10.1 Part Numbers Addressed by This Specification

Table 20 provides the ordering information for the MPC755 parts described in this specification.

#### **Table 20. Part Numbering Nomenclature**

XPC	755	X	XX	nnn	X	X
Product Code	Part Identifier	Process Descriptor	Package	Processor Frequency	Application Modifier	Revision Level
XPC <sup>1</sup>	755	B = HiP4DP	RX = CBGA	400	L: 2.0 V ±100 mV	E: 2.8; PVR = 0008 3203
			PX = PBGA		0° to 105°C	
	755	C = HiP4DP	RX = CBGA	450		

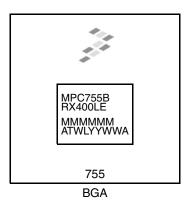
#### Notes:

<sup>1.</sup> The X prefix in a Freescale part number designates a "Pilot Production Prototype" as defined by Freescale SOP 3-13. These are from a limited production volume of prototypes manufactured, tested, and Q.A. inspected on a qualified technology to simulate normal production. These parts have only preliminary reliability and characterization data. Before pilot production prototypes may be shipped, written authorization from the customer must be on file in the applicable sales office acknowledging the qualification status and the fact that product changes may still occur while shipping pilot production prototypes.



## 10.3 Part Marking

Parts are marked as the example shown in Figure 29.



#### Notes:

MMMMMM is the 6-digit mask number.

ATWLYYWWA is the traceability code.

CCCCC is the country of assembly. This space is left blank if parts are assembled in the United States.

Figure 29. Part Marking for BGA Device



# **Document Revision History**

Table B provides a revision history for this hardware specifications addendum.

**Table B. Document Revision History** 

Rev. No.	Date	Editor/ Writer	Substantive Change(s)
0.1	02/15/2006	BM/NB	Changed document order number (was MPC755XLEPNS, Rev. 0). Updated to Freescale template. Updated section numbers to match the hardware specifications document.
0			Initial release.





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