# LCD EMI Filter Array with ESD Protection

#### Description

The CSPEMI606 and CSPEMI608 are EMI filter arrays with ESD protection, which integrate six and eight Pi-filters (C-R-C), respectively. The CSPEMI606/608 has component values of 15 pF – 100  $\Omega$  – 15 pF. These devices include ESD protection diodes on every pin, which provide a very high level of protection for sensitive electronic components that may be subjected to electrostatic discharge (ESD). The ESD diodes connected to the filter ports are designed and characterized to safely dissipate ESD strikes of ±15 kV, beyond the maximum requirement of the IEC 61000–4–2 international standard. Using the MIL–STD–883 (Method 3015) specification for Human Body Model (HBM) ESD, the pins are protected for contact discharges at greater than ±30 kV.

These devices are particularly well suited for portable electronics (e.g. wireless handsets, PDAs, notebook computers) because of their small package format and easy-to-use pin assignments. They are ideal for EMI filtering and protecting data lines from ESD for the LCD display in clamshell handsets. The CSPEMI606 and CSPEMI608 are available in space-saving, low-profile chip-scale packages.

#### Features

- Six and Eight Channels of EMI Filtering
- ±15 kV ESD Protection on each Channel (IEC 61000-4-2 Level 4, Contact Discharge)
- $\pm 30 \text{ kV}$  ESD Protection on each Channel (HBM)
- Better than 30 dB of Attenuation at 1 GHz to 3 GHz
- 15–Bump, 2.960 mm x 1.330 mm Footprint Chip Scale Package (CSPEMI606)
- 20-Bump, 4.000 mm x 1.458 mm Footprint Chip Scale Package (CSPEMI608)
- Chip Scale Package Features Extremely Low Lead Inductance for Optimum Filter and ESD Performance
- These Devices are Pb-Free and are RoHS Compliant

#### Applications

- LCD Data Lines in Clamshell Wireless Handsets
- EMI Filtering & ESD Protection for High-Speed I/O Data Ports
- Wireless Handsets / Cell Phones
- Notebook Computers
- PDAs / Handheld PCs
- EMI Filtering for High-Speed Data Lines



# **ON Semiconductor®**

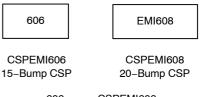
http://onsemi.com



WLCSP15 CASE 567BS

WLCSP20 CASE 567BZ

#### MARKING DIAGRAM



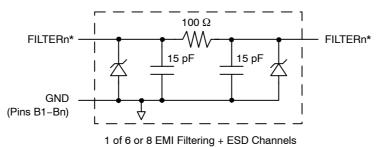
606 = CSPEMI606 EMI608 = CSPEMI608

#### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
CSPEMI606G	CSP-15 (Pb-Free)	3500/Tape & Reel
CSPEMI608G	CSP-20 (Pb-Free)	3500/Tape & Reel

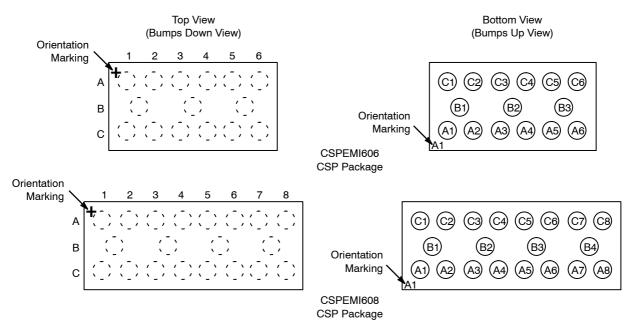
+ For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

#### **ELECTRICAL SCHEMATIC**



\*See Package/Pinout Diagrams for expanded pin information.

#### PACKAGE / PINOUT DIAGRAMS



#### Table 1. PIN DESCRIPTIONS

Pir	n(s)				Pin(s)			
CSPEMI606	CSPEMI608	Name	Description	(	CSPEMI606	CSPEMI608	Name	Description
A1	A1	FILTER1	Filter Channel 1		C1	C1	FILTER1	Filter Channel 1
A2	A2	FILTER2	Filter Channel 2		C2	C2	FILTER2	Filter Channel 2
A3	A3	FILTER3	Filter Channel 3		C3	C3	FILTER3	Filter Channel 3
A4	A4	FILTER4	Filter Channel 4		C4	C4	FILTER4	Filter Channel 4
A5	A5	FILTER5	Filter Channel 5		C5	C5	FILTER5	Filter Channel 5
A6	A6	FILTER6	Filter Channel 6		C6	C6	FILTER6	Filter Channel 6
-	A7	FILTER7	Filter Channel 7		-	C7	FILTER7	Filter Channel 7
-	A8	FILTER8	Filter Channel 8		-	C8	FILTER8	Filter Channel 8
B1-B3	B1-B4	GND	Device Ground					

#### SPECIFICATIONS

#### **Table 2. ABSOLUTE MAXIMUM RATINGS**

Parameter	Rating	Units
Storage Temperature Range	-65 to +150	°C
DC Power per Resistor	100	mW
DC Package Power Rating	500	mW

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

#### **Table 3. STANDARD OPERATING CONDITIONS**

Parameter	Rating	Units
Operating Temperature Range	-40 to +85	°C

#### Table 4. ELECTRICAL OPERATING CHARACTERISTICS (Note 1)

Symbol	Parameter	Conditions	Min	Тур	Max	Units
R	Resistance		80	100	120	Ω
С	Capacitance	At 2.5 V DC, 1 MHz, 30 mV AC	12	15	18	pF
V <sub>DIODE</sub>	Diode Standoff Voltage	I <sub>DIODE</sub> = 10 μA		6.0		V
I <sub>LEAK</sub>	Diode Leakage Current (reverse bias)	V <sub>DIODE</sub> = 3.3 V			200	nA
V <sub>SIG</sub>	Signal Voltage Positive Clamp Negative Clamp	I <sub>LOAD</sub> = 10 mA	5.6 -1.5	6.8 -0.8	9.0 -0.4	V
V <sub>ESD</sub>	In-system ESD Withstand Voltage a) Human Body Model, MIL-STD-883, Method 3015 b) Contact Discharge per IEC 61000-4-2 Level 4	(Notes 2 and 4)	±30 ±15			kV
V <sub>CL</sub>	Clamping Voltage during ESD Discharge MIL–STD–883 (Method 3015), 8 kV Positive Transients Negative Transients	(Notes 2, 3 and 4)		+12 -7		V
f <sub>C</sub>	Cut–off Frequency $Z_{SOURCE}$ = 50 $\Omega$ , $Z_{LOAD}$ = 50 $\Omega$	R = 100 Ω, C = 15 pF		120		MHz

T<sub>A</sub> = 25°C unless otherwise specified.
ESD applied to input and output pins with respect to GND, one at a time.

Clamping voltage is measured at the opposite side of the EMI filter to the ESD pin. For example, if ESD is applied to Pin A1, then clamping voltage is measured at Pin C1.

4. Unused pins are left open.

#### **PERFORMANCE INFORMATION**



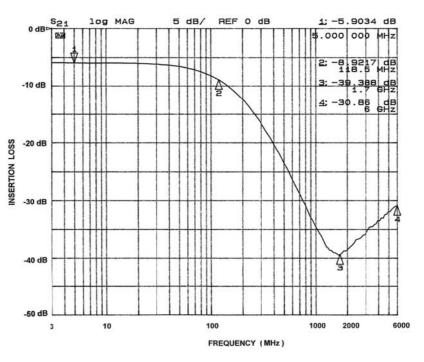


Figure 1. Insertion Loss vs. Frequency (A1-C1 to GND B1)

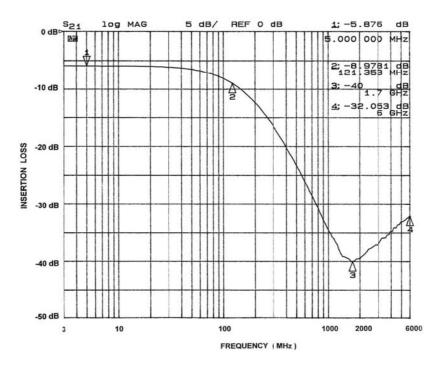
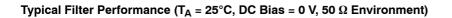


Figure 2. Insertion Loss vs. Frequency (A2-C2 to GND B1)

#### **PERFORMANCE INFORMATION (Cont'd)**



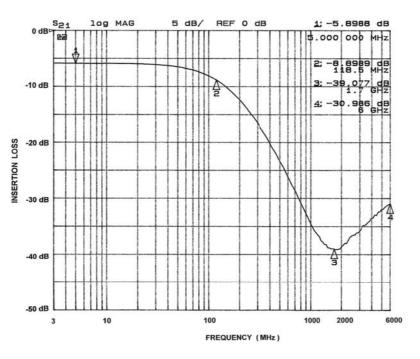


Figure 3. Insertion Loss vs. Frequency (A3-C3 to GND B2)

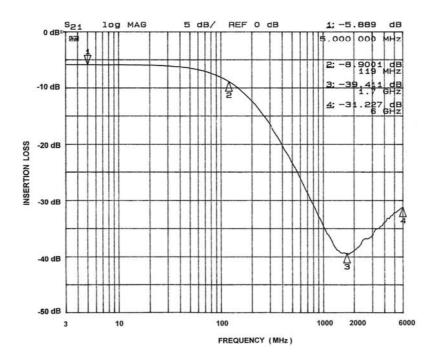
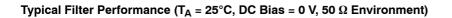


Figure 4. Insertion Loss vs. Frequency (A4-C4 to GND B2)

#### **PERFORMANCE INFORMATION (Cont'd)**



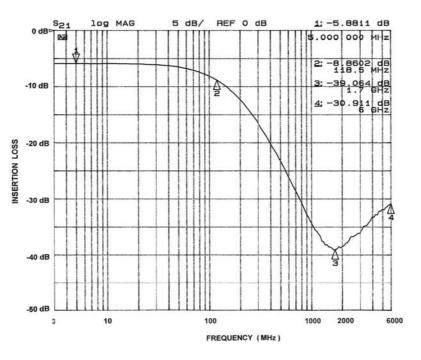


Figure 5. Insertion Loss vs. Frequency (A5-C5 to GND B3)

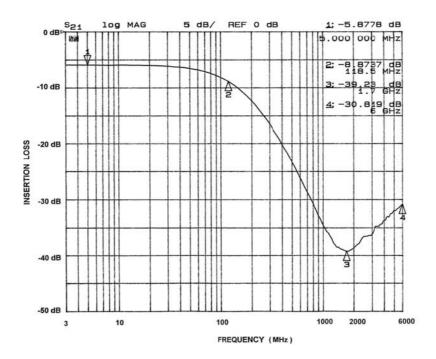
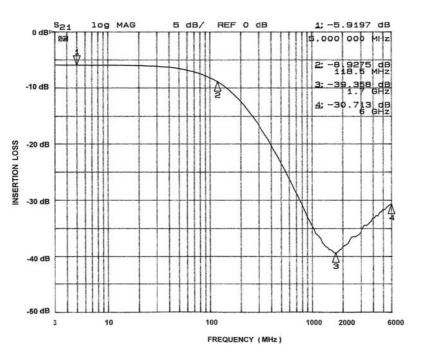
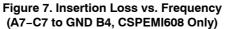


Figure 6. Insertion Loss vs. Frequency (A6-C6 to GND B3)

#### **PERFORMANCE INFORMATION (Cont'd)**

#### Typical Filter Performance (T<sub>A</sub> = 25°C, DC Bias = 0 V, 50 $\Omega$ Environment)





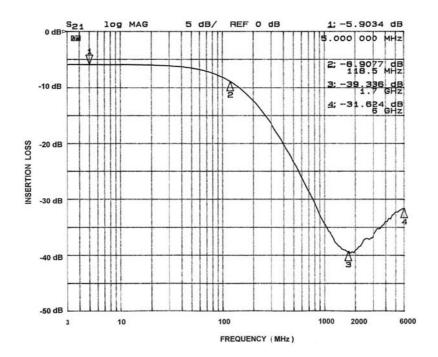


Figure 8. Insertion Loss vs. Frequency (A8-C8 to GND B4, CSPEMI608 Only)

## **PERFORMANCE INFORMATION (Cont'd)**

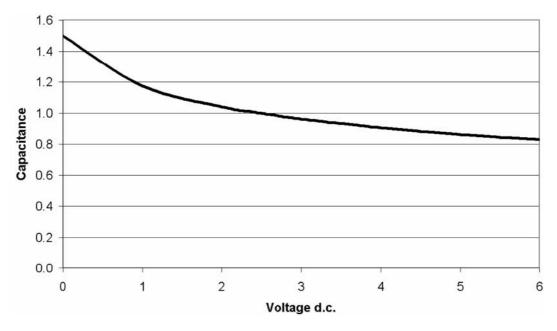


Figure 9. Filter Capacitance vs. Input Voltage over Temperature (normalized to capacitance at 2.5 VDC and 25°C)

#### **APPLICATION INFORMATION**

Parameter	Value		
Pad Size on PCB	0.240 mm		
Pad Shape	Round		
Pad Definition	Non-Solder Mask defined pads		
Solder Mask Opening	0.290 mm Round		
Solder Stencil Thickness	0.125 mm – 0.150 mm		
Solder Stencil Aperture Opening (laser cut, 5% tapered walls)	0.300 mm Round		
Solder Flux Ratio	50/50 by volume		
Solder Paste Type	No Clean		
Pad Protective Finish	OSP (Entek Cu Plus 106A)		
Tolerance – Edge To Corner Ball	±50 μm		
Solder Ball Side Coplanarity	±20 μm		
Maximum Dwell Time Above Liquidous	60 seconds		
Maximum Soldering Temperature for Lead-free Devices using a Lead-free Solder Paste	260°C		

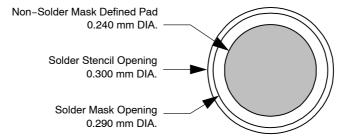


Figure 10. Recommended Non–Solder Mask Defined Pad Illustration

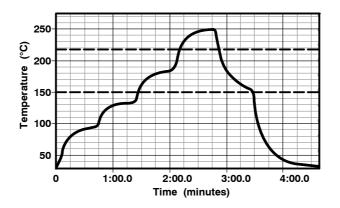
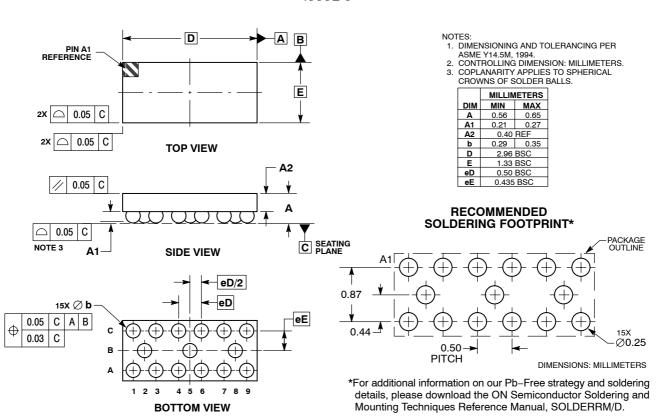


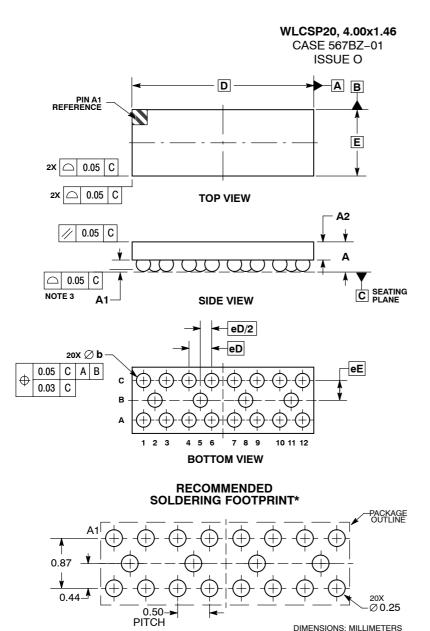
Figure 11. Lead-free (SnAgCu) Solder Ball Reflow Profile

#### PACKAGE DIMENSIONS

WLCSP15, 2.96x1.33 CASE 567BS-01 ISSUE O



#### PACKAGE DIMENSIONS



NOTES:

 DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
CONTROLLING DIMENSION: MILLIMETERS.

 COPLANARITY APPLIES TO SPHERICAL CROWNS OF SOLDER BALLS.

	MILLIMETERS			
DIM	MIN	MAX		
Α	0.56	0.65		
A1	0.21	0.27		
A2	0.40 REF			
b	0.29	0.35		
D	4.00 BSC			
E	1.46 BSC 0.50 BSC 0.435 BSC			
eD				
еE				

\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability, arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other application in which the failure of the SCILLC product create a stuation where personal injury or death may occur. Should Buyer purchase or uses SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use persor to all applicable copyright laws and is not for resale in any manner.

#### PUBLICATION ORDERING INFORMATION

#### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303–675–2175 or 800–344–3860 Toll Free USA/Canada Fax: 303–675–2176 or 800–344–3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800–282–9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support:

Phone: 421 33 790 2910 Japan Customer Focus Center Phone: 81-3-5773-3850 ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative