

ON Semiconductor®

## FDD8444-F085

# N-Channel PowerTrench® MOSFET 40V, 50A, $5.2m\Omega$

### **Features**

- Typ  $R_{DS(on)} = 4m\Omega$  at  $V_{GS} = 10V$ ,  $I_D = 50A$
- Typ  $Q_{q(10)} = 89nC$  at  $V_{GS} = 10V$ ,  $I_D = 50A$
- Low Miller Charge
- Low Qrr Body Diode
- UIS Capability (Single Pulse/ Repetitive
- RoHS Compliant
- Qualified to AEC Q101

## **Applications**

- Automotive Engine Control
- Powertrain Management
- Solenoid and Motor Drivers
- Electronic Transmission
- Distributed Power Architecture and VRMs

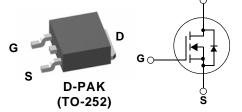


Symbol	Parameter	Ratings	Units		
$V_{DSS}$	Drain to Source Voltage		40	V	
$V_{GS}$	Gate to Source Voltage	±20	V		
ı	Drain Current Continuous (V <sub>GS</sub> = 10V)		50	^	
ID	Pulsed		Figure 4	A	
E <sub>AS</sub>	Single Pulse Avalanche Energy (N	lote 1)	535	mJ	
D	Power Dissipation		153	W	
$P_D$	Derate above 25°C		1.02	W/°C	
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature		-55 to +175	°C	
$R_{\theta JC}$	Thermal Resistance Junction to Case		0.98	°C/W	
$R_{\theta JA}$	Thermal Resistance Junction to Ambient, 1in <sup>2</sup> copper pad area		52	°C/W	

# **Package Marking and Ordering Information**

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDD8444	FDD8444-F085	TO-252AA	13"	12mm	2500 units

- 1: Starting T<sub>J</sub> = 25°C, L = 0.67mH, I<sub>AS</sub> = 40A 2: A suffix as "...F085P" has been temporarily introduced in order to manage a double source strategy as ON Semiconductor has officially animo Au qe2014.



Units

Max

Тур

# **Electrical Characteristics** $T_A = 25^{\circ}C$ unless otherwise noted

**Parameter** 

Off Characteristics							
$B_{VDSS}$	Drain to Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS}$	3 = 0V	40	-	-	V
1	Zero Gate Voltage Drain Current	$V_{DS} = 32V,$		-	-	1	μА
IDSS	Zero Gate voltage Drain Current	$V_{GS} = 0V$	$T_A = 150^{\circ}C$	-	-	250	μΑ
loco	Gate to Source Leakage Current	$V_{00} = +20V$		_	_	+100	nΔ

**Test Conditions** 

Min

## **On Characteristics**

Symbol

$V_{GS(th)}$	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 250 \mu A$	2	2.5	4	V
		$I_D = 50A, V_{GS} = 10V$	1	4	5.2	
r <sub>DS(on)</sub>	Drain to Source On Resistance	$I_D = 50A, V_{GS} = 10V$ $T_J = 175$ °C	-	7.2	9.4	mΩ

## **Dynamic Characteristics**

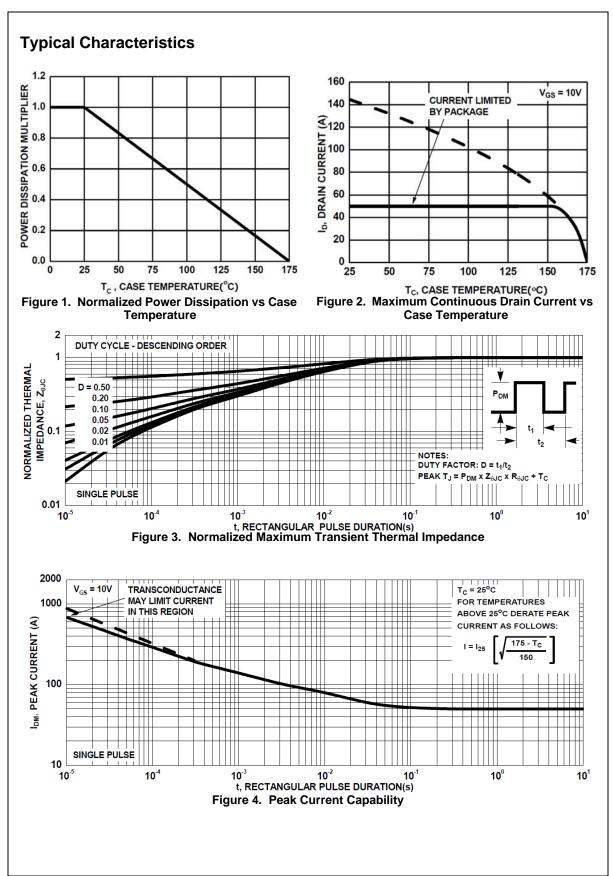
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V, f = 1MHz		-	6195	-	рF
Coss	Output Capacitance			-	585		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			-	332		pF
$R_G$	Gate Resistance	f = 1MHz		•	1.9	1	Ω
$Q_{g(TOT)}$	Total Gate Charge at 10V	$V_{GS} = 0$ to 10V		-	89	116	nC
$Q_{g(TH)}$	Threshold Gate Charge	$V_{GS} = 0$ to $2V$	$V_{DD} = 20V$ $I_{D} = 50A$		11		nC
Q <sub>gs</sub>	Gate to Source Gate Charge			-	23	-	nC
$Q_{gd}$	Gate to Drain "Miller" Charge		-	20	-	nC	

# **Switching Characteristics**

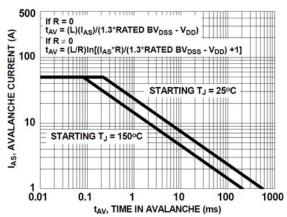
t <sub>on</sub>	Turn-On Time	$V_{DD} = 20V, I_{D} = 50A$ $V_{GS} = 10V, R_{GS} = 2\Omega$	-	-	135	ns
t <sub>d(on)</sub>	Turn-On Delay Time		-	12	-	ns
t <sub>r</sub>	Rise Time		-	78	-	ns
t <sub>d(off)</sub>	Turn-Off Delay Time		-	48	-	ns
t <sub>f</sub>	Fall Time		-	15	-	ns
t <sub>off</sub>	Turn-Off Time		-	-	95	ns

## **Drain-Source Diode Characteristics**

$V_{SD}$	Source to Drain Diode Voltage	I <sub>SD</sub> = 50A	-	-	1.25	V
	Source to Drain blode voltage	$I_{SD} = 25A$	-	-	1.0	
t <sub>rr</sub>	Reverse Recovery Time	1 - 500 dl /dt - 1000/	-	39	51	ns
$Q_{rr}$	Reverse Recovery Charge	$I_{SD} = 50A$ , $dI_{SD}/dt = 100A/\mu s$	-	45	59	nC

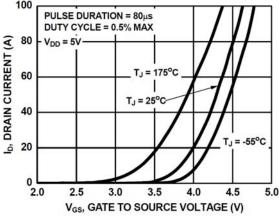


## **Typical Characteristics** 1000 10us 100 DRAIN CURRENT 100us 10 **CURRENT LIMITED** 1 ò OPERATION IN THIS 10<sub>ms</sub> AREA MAY BE LIMITED BY rDS(on) TJ = MAX RATED Tc = 25°C DC 0.1 100 VDS, DRAIN TO SOURCE VOLTAGE (V) Figure 5. Forward Bias Safe Operating Area 100 PULSE DURATION = 80µs



NOTE: Refer to ON Semiconductor Application Notes AN7514 and AN7515

Figure 6. Unclamped Inductive Switching Capability



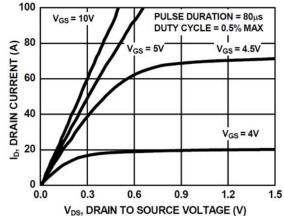
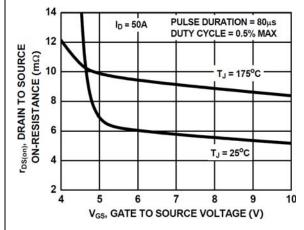


Figure 7. Transfer Characteristics

Figure 8. Saturation Characteristics



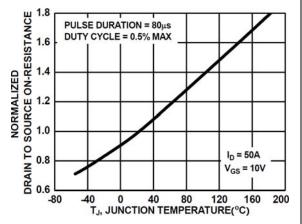


Figure 9. Drain to Source On-Resistance Variation vs Gate to Source Voltage

Figure 10. Normalized Drain to Source On Resistance vs Junction Temperature

## **Typical Characteristics**

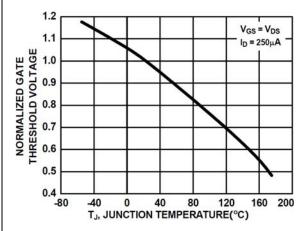


Figure 11. Normalized Gate Threshold Voltage vs
Junction Temperature

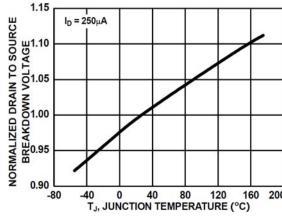


Figure 12. Normalized Drain to Source Breakdown Voltage vs Junction Temperature

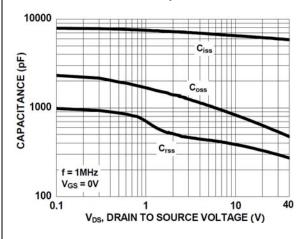


Figure 13. Capacitance vs Drain to Source Voltage

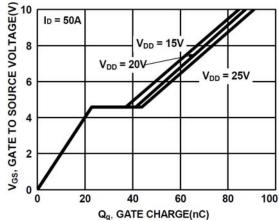


Figure 14. Gate Charge vs Gate to Source Voltage

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