

ON Semiconductor®

FDH5500-F085

N-Channel UltraFET Power MOSFET

55V, 75A, 7m Ω

Features

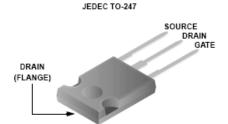
- Typ $r_{DS(on)}$ = 5.2m Ω at V_{GS} = 10V, I_D = 75A
- Typ $Q_{g(10)}$ = 118nC at V_{GS} = 10V
- Simulation Models
 - -Temperature Compensated PSPICE and SABERTM
- Peak Current vs Pulse Width Curve
- UIS Rating Curve
- Related Literature
 - -TB334, "Guidelines for Soldering Surface Mount Componets to PC Boards"
- Qualified to AEC Q101
- RoHS Compliant

Applications

- DC Linear Mode Control
- Solenoid and Motor Control
- Switching Regulators
- Automotive Systems



Package Symbol





MOSFET Maximum Ratings $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter		Ratings	Units
V_{DSS}	Drain to Source Voltage	(Note 1)	55	V
V_{DGR}	Drain to Gate Voltage ($R_{GS} = 20k\Omega$)	(Note 1)	55	V
V _{GS}	Gate to Source Voltage		±20	V
	Drain Current Continuous (T _C < 135°C, V _{GS} = 10V)		75	Α
ID	Pulsed		See Figure 4	7
E _{AS}	Single Pulse Avalanche Energy	(Note 2)	864	mJ
D	Power Dissipation		375	W
P_D	Dreate above 25°C		2.5	W/°C
T _J , T _{STG}	Operating and Storage Temperature		-55 to + 175	
T _L	Max. Lead Temp. for Soldering (at 1.6mm from case for 10sec)		300	°C
T _{pkg}	Max. Package Temp. for Soldering (Package Body for 10sec)		260	

Thermal Characteristics

$R_{\theta JC}$	Thermal Resistance Junction to Case	0.4	°C/W
$R_{\theta JA}$	Thermal Resistance Junction to Ambient TO-247, 1in ² copper pad area	30	°C/W

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDH5500	FDH5500-F085	TO-247	Tube	N/A	30 units

Electrical Characteristics $T_C = 25^{\circ}C$ unless otherwise noted

Symb	I Parameter	Test Conditions	Min	Тур	Max	Units
Off C	aracteristics					
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B _{VDSS}	Drain to Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0$	V	55	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 50V, V_{GS} = 0$	V	-	-	1	^
		$V_{DS} = 45V$	$T_{\rm C} = 150^{\rm o}{\rm C}$	-	-	250	μΑ
I _{GSS}	Gate to Source Leakage Current	$V_{GS} = \pm 20V$		-	-	±100	nA

On Characteristics

$V_{GS(th)}$	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 250 \mu A$	2	2.9	4	V
r _{DS(on)}	Drain to Source On Resistance	I _D = 75A, V _{GS} = 10V	-	5.2	7	mΩ

Dynamic Characteristics

C _{iss}	Input Capacitance	V 05V V	V _{DS} = 25V, V _{GS} = 0V, f = 1MHz	-	3565	-	pF
C _{oss}	Output Capacitance			-	1310	-	pF
C _{rss}	Reverse Transfer Capacitance	1 - 1101112		-	395	-	pF
$Q_{g(TOT)}$	Total Gate Charge at 20V	$V_{GS} = 0$ to 20V		-	206	268	nC
Q _{g(10)}	Total Gate Charge at 10V	$V_{GS} = 0$ to 10V	$V_{DD} = 30V$	-	118	153	nC
$Q_{g(TH)}$	Threshold Gate Charge	$V_{GS} = 0$ to 2V	$I_D = 75A$ $R_1 = 0.4\Omega$	-	6.2	8.1	nC
Q_{gs}	Gate to Source Gate Charge		$I_0 = 1.0 \text{mA}$	-	17.8	-	nC
Q_{gd}	Gate to Drain "Miller" Charge		9	-	51	-	nC

Electrical Characteristics $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units

Switching Characteristics

t _{on}	Turn-On Time		-	-	185	ns
t _{d(on)}	Turn-On Delay Time		-	13.7	-	ns
t _r	Rise Time	$V_{DD} = 30V, I_D = 75A,$ $-R_L = 0.4\Omega, V_{GS} = 10V,$	-	102	-	ns
t _{d(off)}	Turn-Off Delay Time	$R_{\text{L}} = 0.4\Omega, V_{\text{GS}} = 10V,$ $R_{\text{GS}} = 2.5\Omega$	-	34	-	ns
t _f	Fall Time	11GS - 2.032	-	22	-	ns
t _{off}	Turn-Off Time		-	-	91	ns

Drain-Source Diode Characteristics

V_{SD}	Source to Drain Diode Voltage	I _{SD} = 75A	-	1	1.25	V
t _{rr}	Reverse Recovery Time	I = 75A dl /dt = 100A/	-	60	78	ns
Q _{rr}	Reverse Recovery Charge	$-I_F = 75A$, $dI_{SD}/dt = 100A/\mu s$	-	77	100	nC

- Starting T_J = 25°C to175°C.
 Starting T_J = 25°C, L = 0.48mH, I_{AS} = 60A

Typical Characteristics 180 **CURRENT LIMITED** DRAIN CURRENT (A) 00 00 051 BY PACKAGE 10V V_{GS} <u>ئ</u> 30 0.0 0 0 25 75 100 125 25 75 100 125 50 150 T_C, CASE TEMPERATURE(°C) T_C, CASE TEMPERATURE(°C) Figure 1. Normalized Power Dissipation vs Case Figure 2. Maximum Continuous Drain Current vs **Temperature Case Temperature** 2 **DUTY CYCLE - DESCENDING ORDER** NORMALIZED THERMAL IMPEDANCE, Z_{eJC} D = 0.500.20 0.10 0.05 0.02 0.01 NOTES: DUTY FACTOR: D = t₁/t₂ PEAK T_J = P_{DM} x $Z_{\theta JA}$ x $R_{\theta JA}$ + T_{C} SINGLE PULSE 0.01 10⁻³ 10⁻⁴ 10⁻² 10⁻⁵ 10⁻¹ 10 t, RECTANGULAR PULSE DURATION(s) Figure 3. Normalized Maximum Transient Thermal Impedance 10000 $T_C = 25^{\circ}C$ $V_{GS} = 10V$ FOR TEMPERATURES ABOVE 25°C DERATE PEAK **CURRENT AS FOLLOWS:** 1000

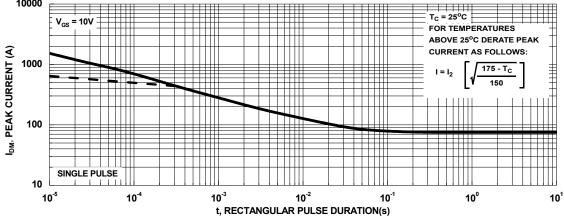


Figure 4. Peak Current Capability

Typical Characteristics

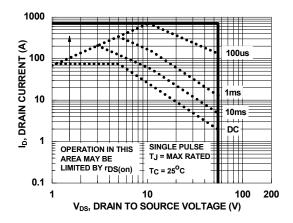


Figure 5. Forward Bias Safe Operating Area

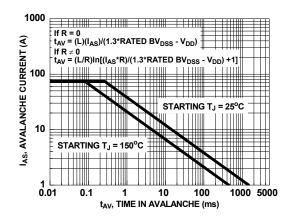


Figure 6. Unclamped Inductive Switching Capability

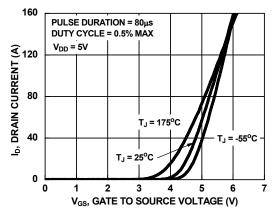


Figure 7. Transfer Characteristics

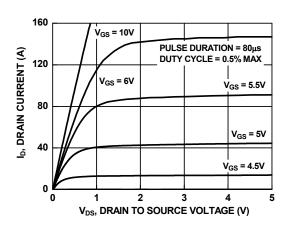


Figure 8. Saturation Characteristics

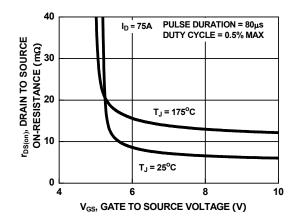


Figure 9. Drain to Source On-Resistance Variation vs Gate to Source Voltage

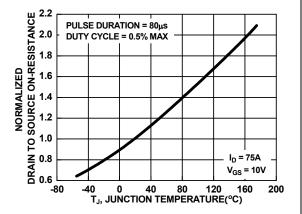


Figure 10. Normalized Drain to Source On Resistance vs Junction Temperature

Typical Characteristics

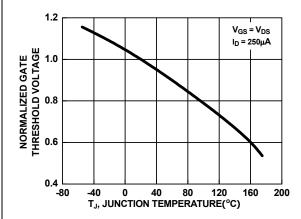


Figure 11. Normalized Gate Threshold Voltage vs Junction Temperature

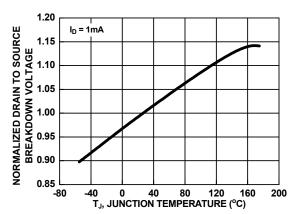


Figure 12. Normalized Drain to Source Breakdown Voltage vs Junction Temperature

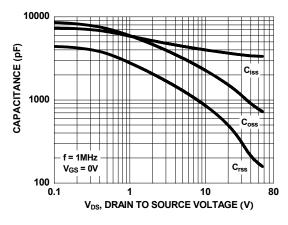


Figure 13. Capacitance vs Drain to Source Voltage

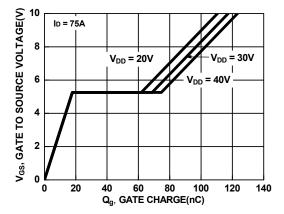


Figure 14. Gate Charge vs Gate to Source Voltage

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