FFSH3065ADN-F155

Silicon Carbide Schottky Diode
650 V, 30 A

Description

Silicon Carbide (SiC) Schottky Diodes use a completely new technology that provides superior switching performance and higher reliability compared to Silicon. No reverse recovery current, temperature independent switching characteristics, and excellent thermal performance sets Silicon Carbide as the next generation of power semiconductor. System benefits include highest efficiency, faster operating frequency, increased power density, reduced EMI, and reduced system size and cost.

Features

- Max Junction Temperature 175°C
- Avalanche Rated 81 mJ
- High Surge Current Capacity
- Positive Temperature Coefficient
- Ease of Paralleling
- No Reverse Recovery / No Forward Recovery

Applications

- General Purpose
- SMPS, Solar Inverter, UPS
- Power Switching Circuits

ON Semiconductor®

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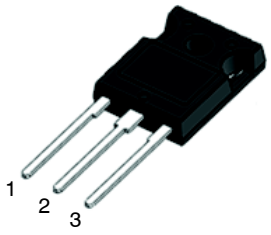
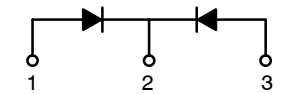
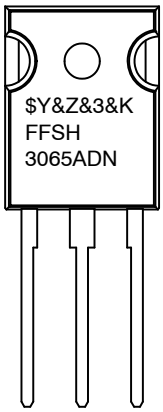


TO−247
LONG LEAD
CASE 340CH



MARKING DIAGRAM



$Y = ON Semiconductor Logo
&Z = Assembly Plant Code
&3 = Numeric Date Code
&K = Lot Code
FFSH3065ADN = Specific Device Code

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

Table 1. ABSOLUTE MAXIMUM RATINGS (T_C = 25°C unless otherwise noted)

Symbol	Parameter		FFSH3065ADN−F155	Unit
V_{RRM}	Peak Repetitive Reverse Voltage		650	V
E_{AS}	Single Pulse Avalanche Energy (Note 1)		81	mJ
I_F	Continuous Rectified Forward Current @ T_C < 114°C		16*/30**	A
	Continuous Rectified Forward Current @ T_C < 135°C		23**/36**	
$I_{F,Max}$	Non−Repetitive Peak Forward Surge Current	T_C = 25°C, 10 μs	1000	A
		T_C = 150°C, 10 μs	900	A
$I_{F,SM}$	Non−Repetitive Forward Surge Current	Half−Sine Pulse, t_p = 8.3 ms	90	A
$I_{F,RM}$	Repetitive Forward Surge Current	Half−Sine Pulse, t_p = 8.3 ms	50	A
Ptot	Power Dissipation	T_C = 25°C	165	W
		T_C = 150°C	28	W
T_J, T_{STG}	Operating and Storage Temperature Range		−55 to +175	°C
	TO247 Mounting Torque, M3 Screw		60	Ncm

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.
1. E_{AS} of 81 mJ is based on starting T_J = 25°C, L = 0.5 mH, I_{AS} = 18 A, V = 50 V.

Table 2. THERMAL CHARACTERISTICS

Symbol	Parameter	Rating	Unit
$R_{\theta JC}$	Thermal Resistance, Junction−to−Case, Max.	0.91*/0.4**	°C/W

* Per Leg
** Per Device

Table 3. OPERATING CHARACTERISTICS (T_C = 25°C, unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
V_F	Forward Voltage	I_F = 16 A, T_C = 25°C	−	1.5	1.75	V
		I_F = 16 A, T_C = 125°C	−	1.6	2.0	
		I_F = 16 A, T_C = 175°C	−	1.72	2.4	
I_R	Reverse Current	V_R = 650 V, T_C = 25°C	−	−	200	μA
		V_R = 650 V, T_C = 125°C	−	−	400	
		V_R = 650 V, T_C = 175°C	−	−	600	
Q_C	Total Capacitive Charge	V = 400 V	−	52	−	nC
C	Total Capacitance	V_R = 1 V, f = 100 kHz	−	887	−	pF
		V_R = 200 V, f = 100 kHz	−	95	−	
		V_R = 400 V, f = 100 kHz	−	72	−	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

PART MARKING AND ORDERING INFORMATION

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FFSH3065ADN−F155	FFSH3065ADN	TO−247 Long Lead	Tube	N/A	N/A	30 units

TYPICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted)

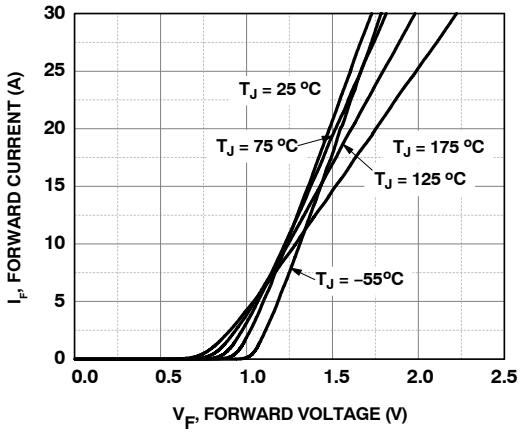


Figure 1. Forward Characteristics

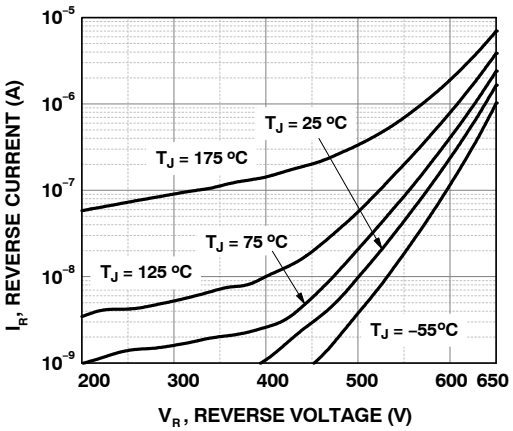


Figure 2. Reverse Characteristics

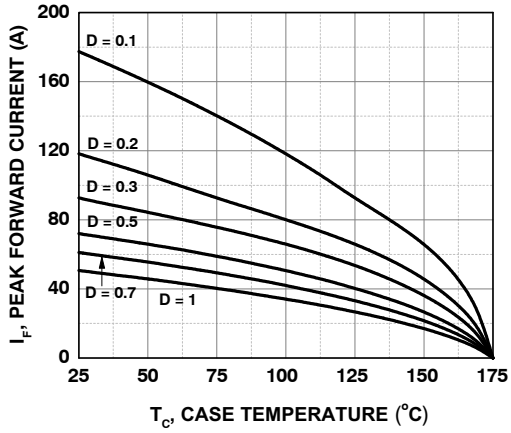


Figure 3. Current Derating

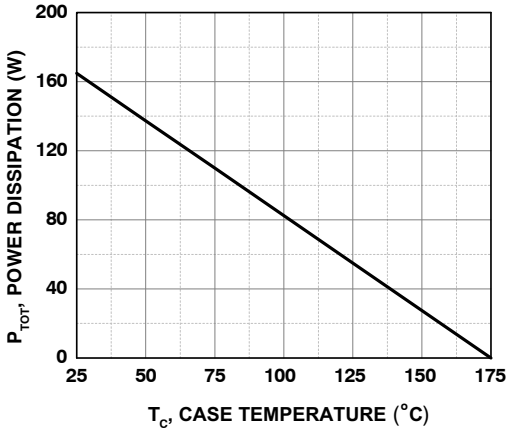


Figure 4. Power Derating

TYPICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted)

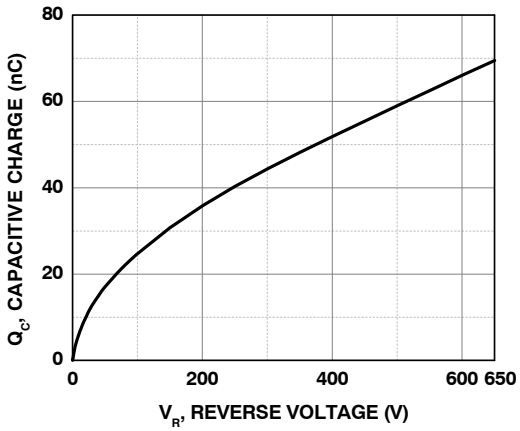


Figure 5. Capacitive Charge vs. Reverse Voltage

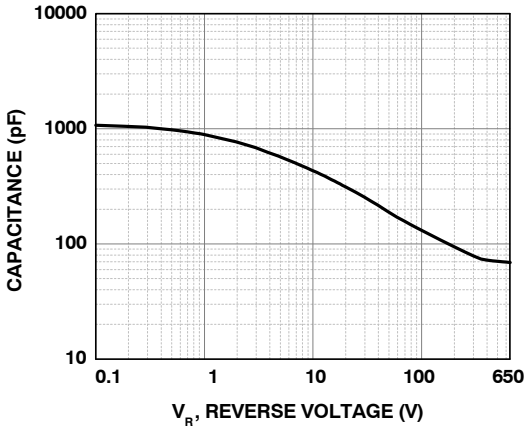


Figure 6. Capacitance vs. Reverse Voltage

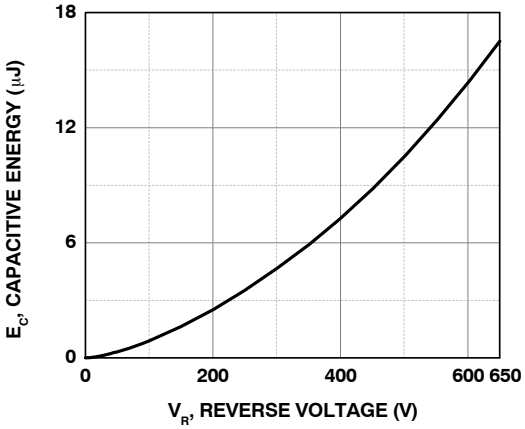


Figure 7. Capacitance Stored Energy

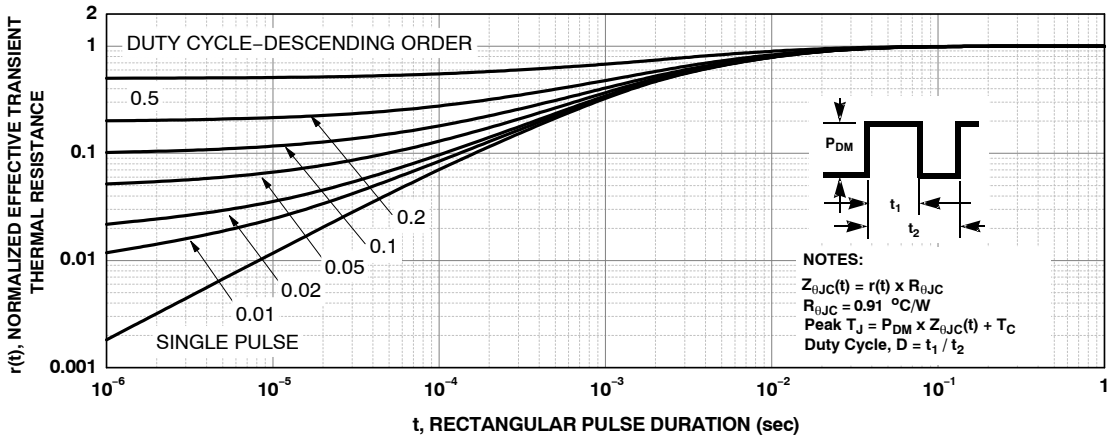


Figure 8. Junction−to−Case Transient Thermal Response Curve

TYPICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted)

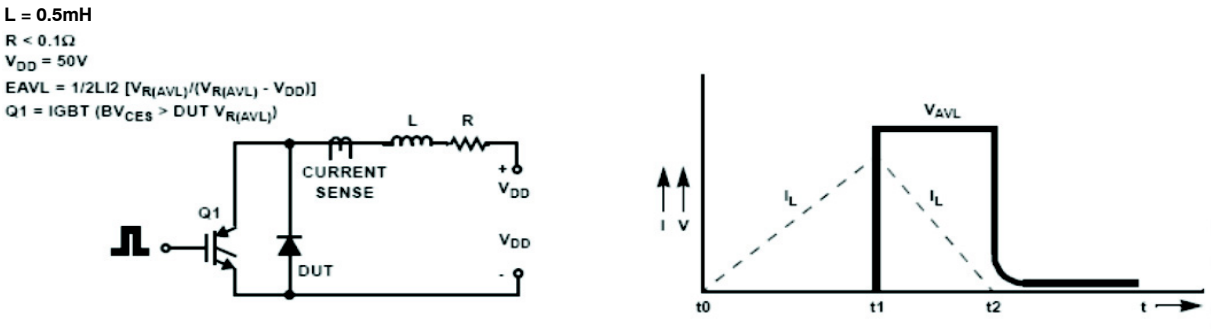
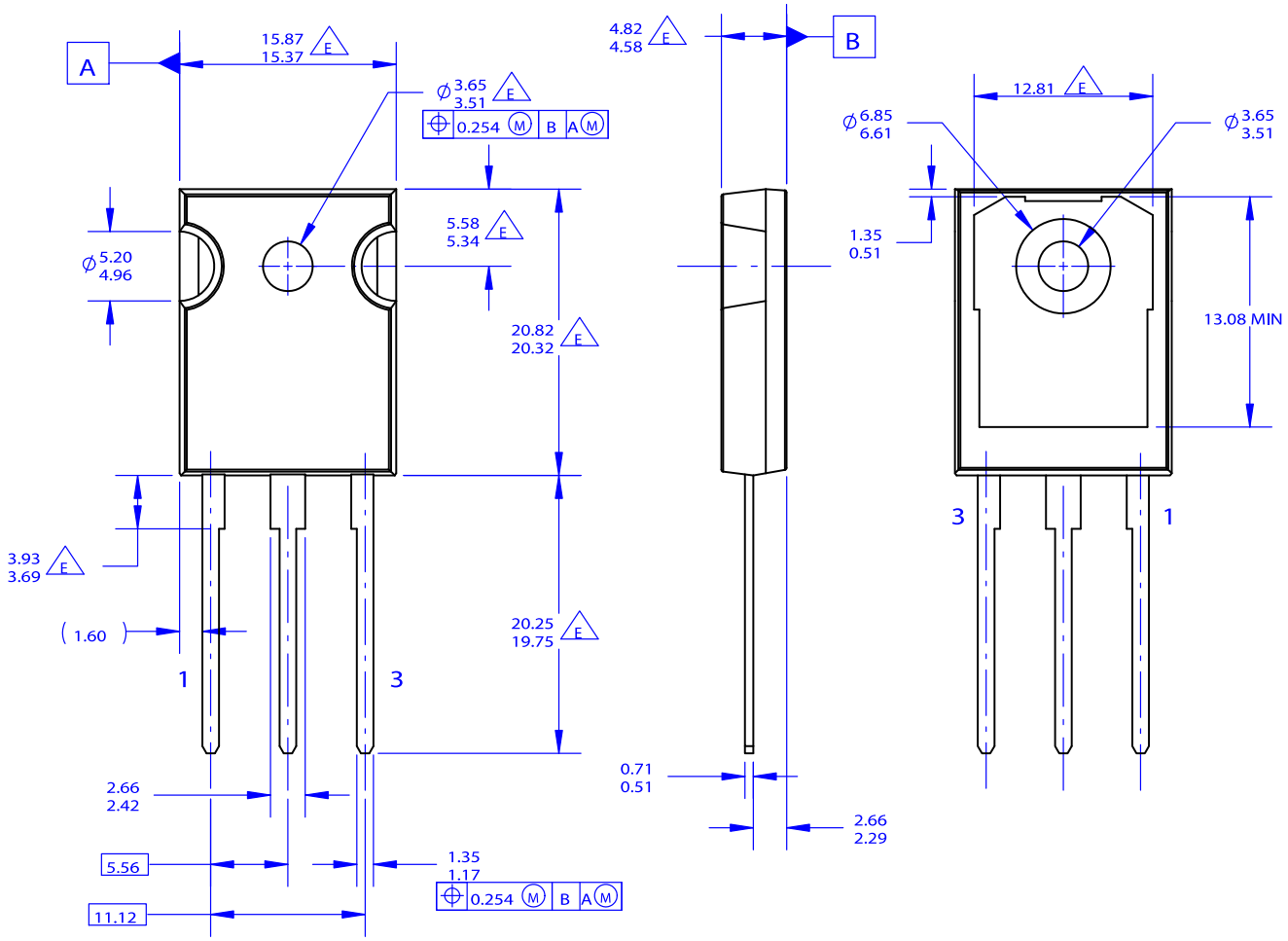


Figure 9. Unclamped Inductive Switching Test Circuit & Waveform

PACKAGE DIMENSIONS

TO−247−3LD
CASE 340CH
ISSUE O



NOTES: UNLESS OTHERWISE SPECIFIED.

A. PACKAGE REFERENCE: JEDEC TO-247, ISSUE E, VARIATION AB, DATED JUNE, 2004.
B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
C. ALL DIMENSIONS ARE IN MILLIMETERS.
D. DRAWING CONFORMS TO ASME Y14.5 - 1994

E DOES NOT COMPLY JEDEC STANDARD VALUE

PUBLICATION ORDERING INFORMATION

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