# Silicon Carbide Schottky Diode

# 650 V, 8 A

Silicon Carbide (SiC) Schottky Diodes use a completely new technology that provides superior switching performance and higher reliability compared to Silicon. No reverse recovery current, temperature independent switching characteristics, and excellent thermal performance sets Silicon Carbide as the next generation of power semiconductor. System benefits include highest efficiency, faster operating frequency, increased power density, reduced EMI, and reduced system size and cost.

#### **Features**

- Max Junction Temperature 175°C
- Avalanche Rated 33 mJ
- High Surge Current Capacity
- Positive Temperature Coefficient
- Ease of Paralleling
- No Reverse Recovery / No Forward Recovery
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

#### **Applications**

- General Purpose
- SMPS, Solar Inverter, UPS
- Power Switching Circuits

# **MAXIMUM RATINGS** (T<sub>J</sub> = 25°C unless otherwise noted)

Parameter		Symbol	Value	Unit
Peak Repetitive Reverse Voltage		$V_{RRM}$	650	V
Single Pulse Avalanche Energy ( $I_{L(pk)} = 11.5 \text{ A}, L = 0.5 \text{ mH}, V = 50$	E <sub>AS</sub>	33	mJ	
Continuous Rectified Forward Current	@ T <sub>C</sub> < 147		8.0	Α
Current	@ T <sub>C</sub> < 135		10.1	
Non-Repetitive Peak Forward Surge Current	$T_C = 25^{\circ}C$ $t_P = 10 \mu s$	I <sub>FM</sub>	551	Α
	$T_C = 150$ °C $t_P = 10 \mu s$		498	
Non-Repetitive Forward Surge Current (Half-Sine Pulse)	$T_{C} = 25^{\circ}C$ $t_{P} = 8.3 \text{ ms}$	I <sub>FSM</sub>	56	Α
Power Dissipation	T <sub>C</sub> = 25°C	P <sub>tot</sub>	73	W
	T <sub>C</sub> = 150°C		12	
Operating Junction and Storage T Range	emperature	T <sub>J</sub> , T <sub>stg</sub>	T <sub>J</sub> , T <sub>stg</sub> -55 to +175	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

#### THERMAL CHARACTERISTICS

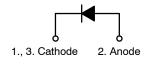
Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case, Max.	$R_{ heta JC}$	2.05	°C/W



#### ON Semiconductor®

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V <sub>RRM</sub>	l <sub>F</sub>		
650 V	8.0 A		

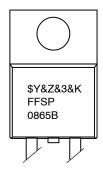


**Schottky Diode** 



**TO-220-2LD CASE 340BB** 

## **MARKING DIAGRAM**



\$Y = ON Semiconductor Logo &Z = Assembly Plant Code &3 = Numeric Date Code &K = Lot Code

FFSP0865B

#### **ORDERING INFORMATION**

= Specific Device Code

See detailed ordering and shipping information on page 2 of this data sheet.

# **ELECTRICAL CHARACTERISTICS** ( $T_J = 25^{\circ}C$ unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
ON CHARACTERISTICS			•	•		•
Forward Voltage	V <sub>F</sub>	I <sub>F</sub> = 8.0 A, T <sub>J</sub> = 25°C		1.39	1.7	V
		I <sub>F</sub> = 8.0 A, T <sub>J</sub> = 125°C		1.55	2.0	1
		I <sub>F</sub> = 8.0 A, T <sub>J</sub> = 175°C		1.71	2.4	
Reverse Current	I <sub>R</sub>	V <sub>R</sub> = 650 V, T <sub>J</sub> = 25°C		0.073	40	μΑ
		V <sub>R</sub> = 650 V, T <sub>J</sub> = 125°C		0.24	80	1
		V <sub>R</sub> = 650 V, T <sub>J</sub> = 175°C		0.48	160	1
CHARGES, CAPACITANCES & C	GATE RESISTANCE		-			
Total Capacitive Charge	$Q_{\mathbb{C}}$	V <sub>C</sub> = 400 V		22		nC
	C <sub>tot</sub>	V <sub>R</sub> = 1 V, f = 100 kHz		336		pF
		V <sub>R</sub> = 200 V, f = 100 kHz		39		1
		V <sub>R</sub> = 400 V, f = 100 kHz		30		

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

## PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Marking	Package	Packing Method	Reel Size	Tape Width	Quantity
FFSP0865B	FFSP0865B	TO220	Tube	N/A	N/A	50 Units

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, <a href="https://example.com/BRD8011/D">BRD8011/D</a>.

# **TYPICAL CHARACTERISTICS**

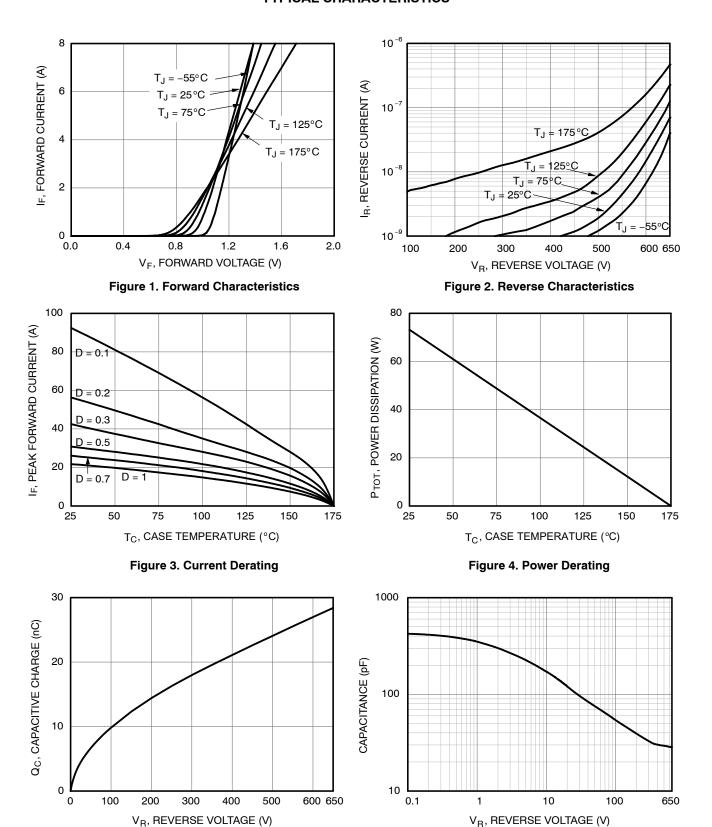


Figure 5. Capacitive Charge vs. Reverse Voltage

Figure 6. Capacitance vs. Reverse Voltage

# **TYPICAL CHARACTERISTICS**

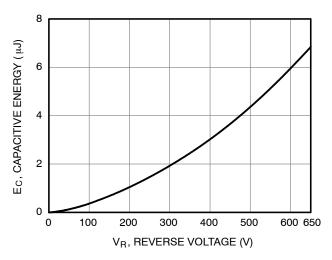


Figure 7. Capacitance Stored Energy

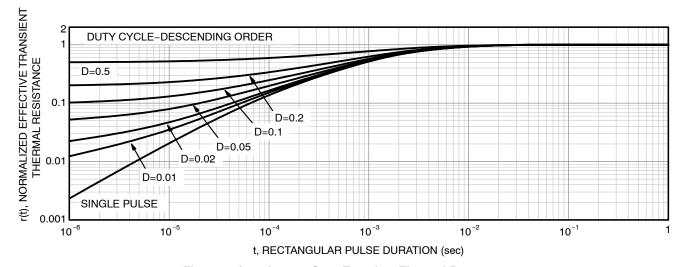
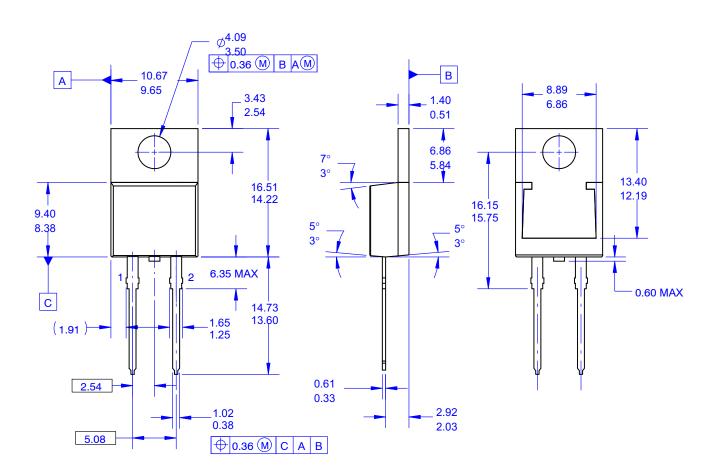
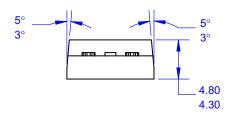


Figure 8. Junction-to-Case Transient Thermal Response

#### **PACKAGE DIMENSIONS**

TO-220-2LD CASE 340BB ISSUE O





#### NOTES:

- A. PACKAGE REFERENCE: JEDEC TO220, ISSUE K, VARIATION AC, DATED APRIL 2002.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DIMENSION AND TOLERANCE AS PER ASME Y14.5–2009. D. DIMENSIONS ARE EXCLUSIVE OF BURRS,
- MOLD FLASH AND TIE BAR PROTRUSIONS.

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## **PUBLICATION ORDERING INFORMATION**

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