Silicon Carbide Schottky Diode

650 V, 30 A

Silicon Carbide (SiC) Schottky Diodes use a completely new technology that provides superior switching performance and higher reliability compared to Silicon. No reverse recovery current, temperature independent switching characteristics, and excellent thermal performance sets Silicon Carbide as the next generation of power semiconductor. System benefits include highest efficiency, faster operating frequency, increased power density, reduced EMI, and reduced system size and cost.

Features

- Max Junction Temperature 175°C
- Avalanche Rated 144 mJ
- High Surge Current Capacity
- Positive Temperature Coefficient
- Ease of Paralleling
- No Reverse Recovery / No Forward Recovery
- AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

Applications

- Automotive HEV-EV Onboard Chargers
- Automotive HEV-EV DC-DC Converters

ABSOLUTE MAXIMUM RATINGS

(T_C = 25°C, Unless otherwise specified)

Symbol	Parar	FF- SP3065B-F085	Unit		
V_{RRM}	Peak Repetitive Rev	650	V		
E _{AS}	Single Pulse Avalan	144	mJ		
	Continuous Rectified @ T _C < 135°C	30			
I _{F, Max}	Non-Repetitive Peak Forward	$T_C = 25^{\circ}C$, 10 µs	1100	Α	
	Surge Current	T _C = 150°C, 10 μs	1000		
I _{F, SM}	Non-Repetitive Forward Surge Current	Half–Sine Pulse, t _p = 8.3 ms	110	Α	
P _{tot}	Power Dissipation	T _C = 25°C	197	W	
	T _C = 150°C		33		
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +175	°C	

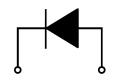
^{1.} E_{AS} of 144 mJ is based on starting $T_J = 25$ °C, L = 0.5 mH, $I_{AS} = 24$ A, V = 50 V.



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ELECTRICAL CONNECTION



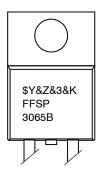
1. Cathode

2. Anode



TO-220-2LD CASE 340BB

MARKING DIAGRAM



\$Y = ON Semiconductor Logo &Z = Assembly Plant Code &3 = Numeric Date Code

kK = Lot Code

FFSP3065B-F085 = Specific Device Code

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

THERMAL CHARACTERISTICS

Symbol	Parameter	Ratings	Unit
$R_{ heta JC}$	Thermal Resistance, Junction to Case, Max.	0.76	°C/W

PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FFSP3065B-F085	FFSP3065B	TO220	Tube	N/A	N/A	50 Units

ELECTRICAL CHARACTERISTICS $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
V _F	Forward Voltage	I _F = 30 A, T _C = 25°C	_	1.38	1.7	V
		I _F = 30 A, T _C = 125°C	_	1.6	2.0	
		I _F = 30 A, T _C = 175°C	-	1.72	2.4	
I _R	Reverse Current	V _R = 650 V, T _C = 25°C	_	0.5	40	μΑ
		V _R = 650 V, T _C = 125°C	-	1.0	80	
		V _R = 650 V, T _C = 175°C	-	2.0	160	
Q_{C}	Total Capacitive Charge	V = 400 V	-	74	-	nC
С	Total Capacitance	V _R = 1 V, f = 100 kHz	-	1280	-	pF
		V _R = 200 V, f = 100 kHz	_	139	-	
		V _R = 400 V, f = 100 kHz	-	108	-	

TYPICAL CHARACTERISTICS $T_J = 25^{\circ}C$ Unless Otherwise Noted

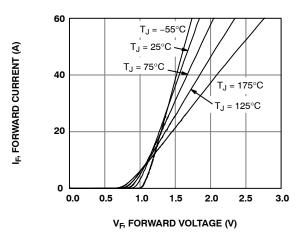


Figure 1. Forward Characteristics

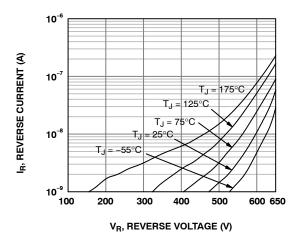


Figure 2. Reverse Characteristics

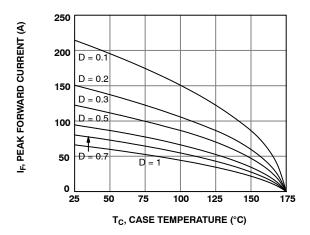


Figure 3. Current Derating

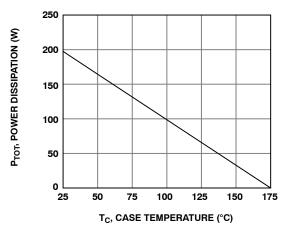


Figure 4. Power Dissipation

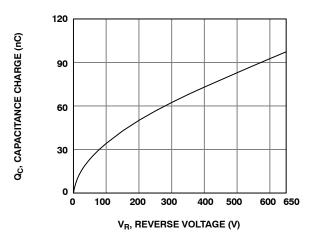


Figure 5. Capacitance Charge vs. Reverse Voltage

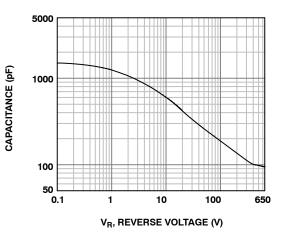


Figure 6. Capacitance vs. Reverse Voltage

TYPICAL CHARACTERISTICS T_J = 25°C Unless Otherwise Noted (continued)

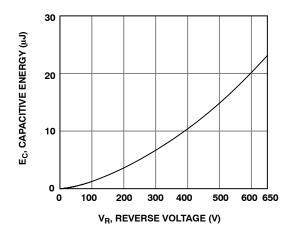


Figure 7. Capacitance Stored Energy

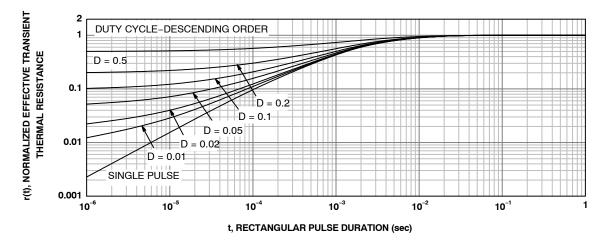


Figure 8. Junction-to-Case Transient Thermal Response Curve

TEST CIRCUIT AND WAVEFORMS

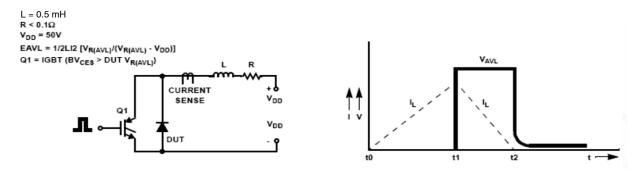
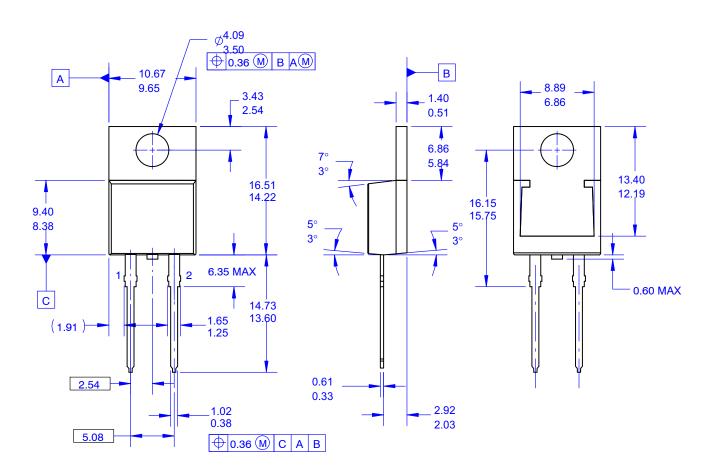
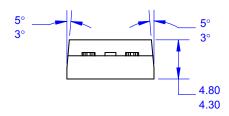


Figure 9. Unclamped Inductive Switching Test Circuit & Waveform

PACKAGE DIMENSIONS

TO-220-2LD CASE 340BB **ISSUE O**





NOTES:

- A. PACKAGE REFERENCE: JEDEC TO220,ISSUE K, VARIATION AC,DATED APRIL 2002. B. ALL DIMENSIONS ARE IN MILLIMETERS. C. DIMENSION AND TOLERANCE AS PER ASME
- Y14.5-2009.
- D. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUSIONS.

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