

# HBL1060BRP

## Product Preview

### 1-Channel ESD Protector

#### Product Description

The HBL1060BRP provides robust ESD protection for sensitive parts that may be subjected to electrostatic discharge (ESD). The tiny form factor means it can be used in very confined spaces. This device is designed and characterized to safely dissipate ESD strikes of at least  $\pm 8$  kV, according to the MIL-STD-883 (Method 3015) specification for Human Body Model (HBM) ESD.

#### Features

- Compact Die Protects from ESD Discharges
- Almost no Conduction at Signal Amplitudes less than  $\pm 50$  V
- ESD Protection to over  $\pm 8$  kV Contact Discharge per MIL\_STD\_883 International ESD Standard
- These Devices are Pb-Free and are RoHS Compliant

#### Applications

- LED Lighting
- Modules
- Interface Circuits



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#### ELECTRICAL SCHEMATIC

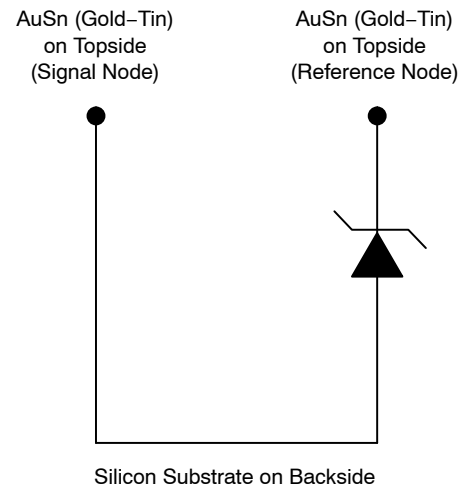


Table 1. ORDERING INFORMATION†

Part Numbering Information					
Ordering Part Number	Topside Metal	Back Metal	BG Thickness	Inking?	Shipping Method
HBL1060BRP	Gold-Tin (AuSn)	None (Si Substrate)	4 mils	No	Die on tape in ring-pack

NOTE: Contact your sales representative for other ordering options.

This document contains information on a product under development. ON Semiconductor reserves the right to change or discontinue this product without notice.

# HBL1060BRP

## SPECIFICATIONS

**Table 2. OPERATING CONDITIONS**

Parameter	Rating	Units
Operating Temperature Range	-40 to +130	°C
Storage Temperature Range	-55 to +130	°C

**Table 3. ELECTRICAL OPERATING CHARACTERISTICS**

Symbol	Parameter	Conditions	Min	Typ	Max	Units
I <sub>LEAK</sub>	Leakage Current	V = 50 V, T = 25°C		±0.1	±1	μA
		V = 54 V, T = 25°C		±10	±100	μA
V <sub>BD</sub>	Breakdown Voltage	T = 25°C at 10.0 mA	55	60	65	V
V <sub>ESD</sub>	ESD Voltage Rating (Note 1) Contact Discharge per Human Body Model, MIL-STD-883 (Method 3015)	T = 25°C	±8			kV
V <sub>CDM</sub>	ESD Voltage Rating under CDM (Note 2)	T = 25°C	500			V
C <sub>T</sub>	Capacitance	T = 25°C		18		pF
	Temp Coefficient of BV	10 mA		70		mV/K
V <sub>F</sub>	Forward Voltage	10 mA	0.6		1.5	V

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

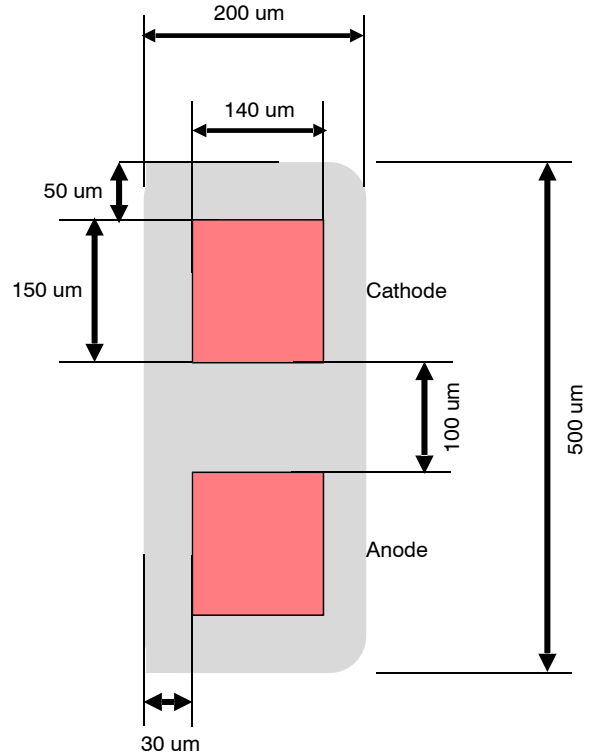
- Per the standard, 3 positive and 3 negative strikes are applied, one second apart.
- Charged Device Model (CDM) ±500 V per JEDEC standard: JESD22-C101.

## MECHANICAL DETAILS

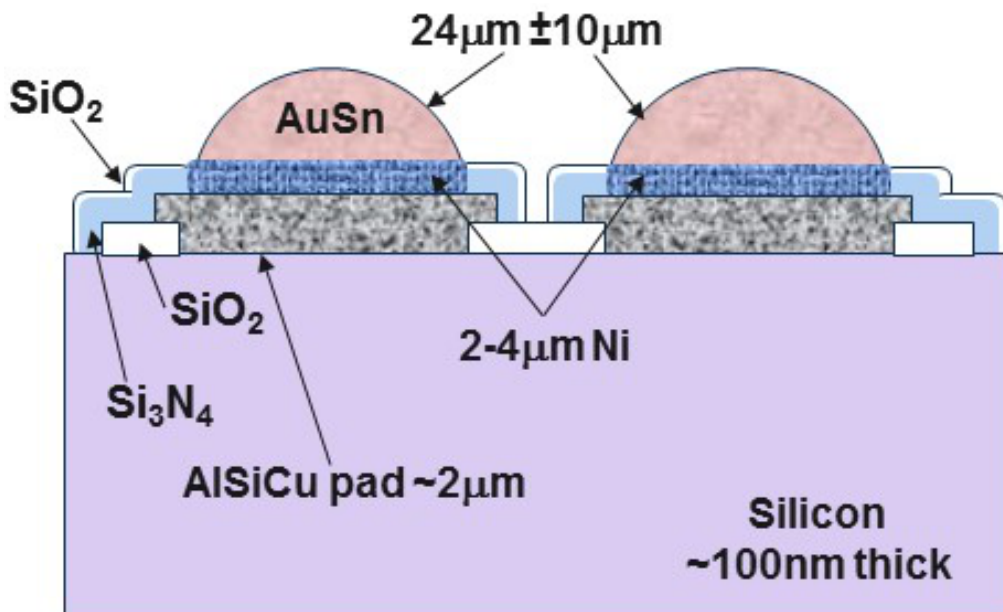
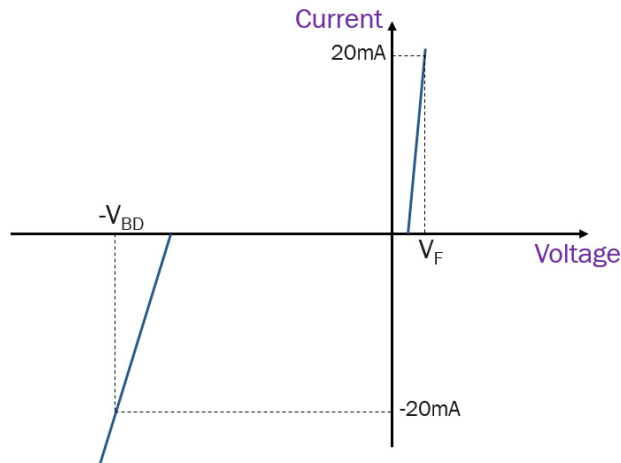
**Table 4. MECHANICAL SPECIFICATIONS** (Note 3)


Symbol	Value	Unit
Composition	Silicon Wafer, P+ doped	
Length (Sawn)	500	μm
Width (Sawn)	200	μm
Thickness	4	mils
Top Pad Length	150	μm
Top Pad Width	140	μm
Top Pad Spacing	100	μm
Top Pad Composition	AuSn (gold-tin)	
Top Pad Thickness	24 ± 10	μm
Back Metal (Underside)	None (silicon substrate)	

3. Dimensions are typical values if tolerances are not specified.



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