

September 2017

ON Semiconductor

## ISL9R3060G2, ISL9R3060P2 30 A, 600 V, STEALTH<sup>TM</sup> Diode

### **Features**

- Stealth Recovery  $t_{rr}$  = 36ns (@  $I_F$  = 30 A)
- Max Forward Voltage, V<sub>F</sub> = 2.4 V (@ T<sub>C</sub> = 25°C)
- 600 V Reverse Voltage and High Reliability
- · Avalanche Energy Rated
- RoHS Compliant

### **Applications**

- SMPS
- · Hard Switched PFC Boost Diode
- · UPS Free Wheeling Diode
- · Motor Drive FWD
- SMPS FWD
- Snubber Diode

### **Description**

The ISL9R3060G2, ISL9R3060P2 is a STEALTH<sup>TM</sup> diode optimized for low loss performance in high frequency hard switched applications. The STEALTH<sup>TM</sup> family exhibits low reverse recovery current (I<sub>rr</sub>) and exceptionally soft recovery under typical operating conditions. This device is intended for use as a free wheeling or boost diode in power supplies and other power switching applications. The low I<sub>rr</sub> and short ta phase reduce loss in switching transistors. The soft recovery minimizes ringing, expanding the range of conditions under which the diode may be operated without the use of additional snubber circuitry. Consider using the STEALTH<sup>TM</sup> diode with an SMPS IGBT to provide the most efficient and highest power density design at lower cost.

# Package JEDEC STYLE 2 LEAD TO-247-2L ANODE CATHODE (BOTTOM SIDE METAL) CATHODE CATHODE CATHODE CATHODE ANODE ANODE CATHODE ANODE ANODE

### Device Maximum Ratings T<sub>C</sub>= 25°C unless otherwise noted

Symbol	Parameter	Ratings	Unit
V <sub>RRM</sub>	Peak Repetitive Reverse Voltage	600	V
V <sub>RWM</sub>	Working Peak Reverse Voltage	600	V
V <sub>R</sub>	DC Blocking Voltage	600	V
I <sub>F(AV)</sub>	Average Rectified Forward Current	30	Α
I <sub>FRM</sub>	Repetitive Peak Surge Current (20kHz Square Wave)	70	Α
I <sub>FSM</sub>	Nonrepetitive Peak Surge Current (Halfwave 1 Phase 60Hz)	325	Α
$P_{D}$	Power Dissipation	200	W
E <sub>AVL</sub>	Avalanche Energy (1A, 40mH)	20	mJ
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range	-55 to 175	°C
TL	Maximum Temperature for Soldering	300	°C
T <sub>PKG</sub>	Leads at 0.063in (1.6mm) from Case for 10s Package Body for 10s, See Techbrief TB334	260	°C

CAUTION: Stresses above those listed in "Device Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

### **Package Marking and Ordering Information**

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
ISL9R3060G2	R3060G2	TO-247-2L	Tube	N/A	N/A	30
ISL9R3060P2	R3060P2	TO-220AC-2L	Tube	N/A	N/A	50

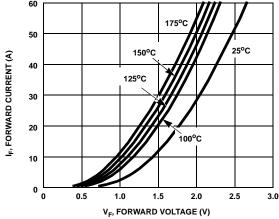
### Electrical Characteristics T<sub>C</sub> = 25°C unless otherwise noted

Symbol	Parameter	Test C	Test Conditions		Тур	Max	Unit
Off State	Characteristics						
I <sub>R</sub>	Instantaneous Reverse Current	V <sub>R</sub> = 600 V	T <sub>C</sub> = 25°C	-	-	100	μΑ
			T <sub>C</sub> = 125°C	-	-	1.0	mA
On State	Characteristics						
V <sub>F</sub>	Instantaneous Forward Voltage	I <sub>F</sub> = 30 A	T <sub>C</sub> = 25°C	-	2.1	2.4	V
			T <sub>C</sub> = 125°C	-	1.7	2.1	V
Dynamic	Characteristics						
CJ	Junction Capacitance	$V_R = 10 \text{ V}, I_F = 0$	V <sub>R</sub> = 10 V, I <sub>F</sub> = 0 A		120	-	pF
Switching	g Characteristics						
t <sub>rr</sub>	Reverse Recovery Time	$I_F = 1 A$ , $di_F/dt = 1$	100 A/μs, V <sub>R</sub> = 30 V	-	27	35	ns
		$I_F=30 A, di_F/dt = 1$	$I_F$ =30 A, $di_F/dt$ = 100 A/ $\mu$ s, $V_R$ = 30 V		36	45	ns
t <sub>rr</sub>	Reverse Recovery Time	I <sub>F</sub> = 30 A,		-	36	-	ns
I <sub>rr</sub>	Reverse Recovery Current	di <sub>F</sub> /dt = 200 A/μs			2.9	-	Α
Q <sub>rr</sub>	Reverse Recovery Charge	V <sub>R</sub> = 390 V, T <sub>C</sub> = 25°C		-	55	-	nC
t <sub>rr</sub>	Reverse Recovery Time	I <sub>F</sub> = 30 A,	$I_F = 30 \text{ A},$ $di_F/dt = 200 \text{ A/}\mu\text{s},$ $V_R = 390 \text{ V},$ $T_C = 125^{\circ}\text{C}$		110	-	ns
S	Softness Factor (t <sub>b</sub> /t <sub>a</sub> )				1.9	-	
I <sub>rr</sub>	Reverse Recovery Current				6	-	Α
Q <sub>rr</sub>	Reverse Recovery Charge	1 <sub>C</sub> = 123 C			450	-	nC
t <sub>rr</sub>	Reverse Recovery Time	I <sub>F</sub> = 30 A,	di <sub>F</sub> /dt = 1000 A/μs,		60	-	ns
S	Softness Factor (t <sub>b</sub> /t <sub>a</sub> )				1.25	-	
I <sub>rr</sub>	Reverse Recovery Current	$V_R = 390 \text{ V},$ $T_C = 125^{\circ}\text{C}$		-	21	-	Α
Q <sub>rr</sub>	Reverse Recovery Charge	1C - 123 C		730	-	nC	
dl <sub>M</sub> /dt	Maximum di/dt during t <sub>b</sub>			-	800	-	A/µ

### **Thermal Characteristics**

$R_{\theta JC}$	Thermal Resistance Junction to Case		-	-	0.75	°C/W
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	TO-247	-	-	30	°C/W
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	TO-220	ı	ı	62	°C/W

# Typical Performance Curves

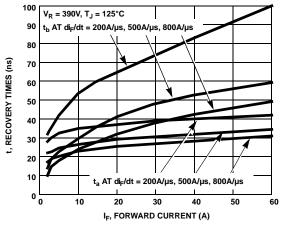


1000 175°C 150°C 150°C 100°C 125°C 100°C 1

5000

Figure 1. Forward Current vs Forward Voltage

Figure 2. Reverse Current vs Reverse Voltage



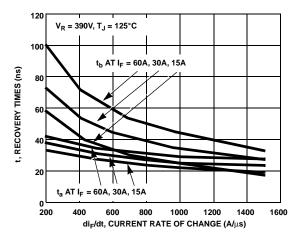
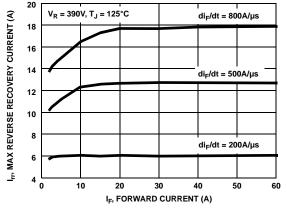


Figure 3. t<sub>a</sub> and t<sub>b</sub> Curves vs Forward Current

Figure 4. t<sub>a</sub> and t<sub>b</sub> Curves vs di<sub>F</sub>/dt



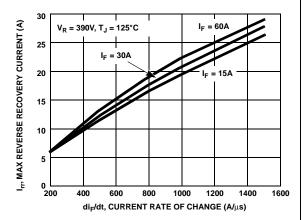


Figure 5. Maximum Reverse Recovery Current vs Forward Current

Figure 6. Maximum Reverse Recovery Current vs di<sub>F</sub>/dt

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Typical Performance Curves (Continued)

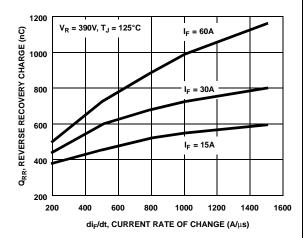
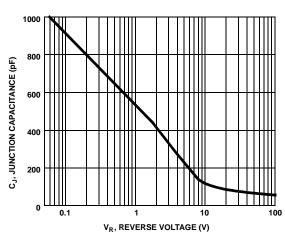


Figure 7. Reverse Recovery Softness Factor vs di<sub>F</sub>/dt

Figure 8. Reverse Recovery Charge vs  $\mathrm{di}_{\mathrm{F}}/\mathrm{dt}$ 



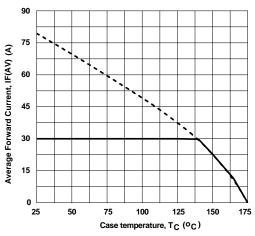


Figure 9. Junction Capacitance vs Reverse Voltage

Figure 10. Forward Current Derating Curve

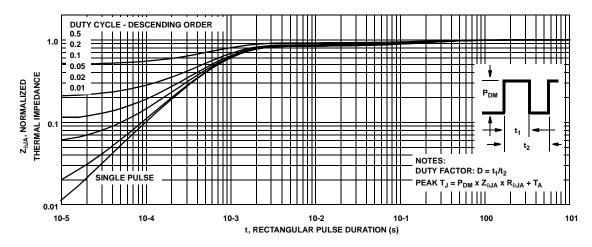
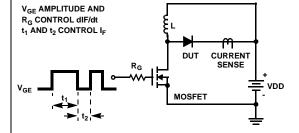


Figure 11. Normalized Maximum Transient Thermal Impedance

### **Test Circuit and Waveforms**



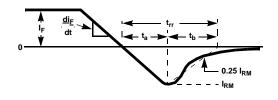


Figure 12. t<sub>rr</sub> Test Circuit

Figure 13.  $t_{rr}$  Waveforms and Definitions

```
I = 1A
L = 40mH
R < 0.1Ω
V<sub>DD</sub> = 50V
EAVL = 1/2L12 [V<sub>R(AVL)</sub>/(V<sub>R(AVL)</sub> - V<sub>DD</sub>)]
Q1 = IGBT (BV<sub>CES</sub> > DUT V<sub>R(AVL)</sub>)

CURRENT
SENSE
V<sub>DD</sub>
V<sub>DD</sub>
V<sub>DD</sub>
DUT
- •
```

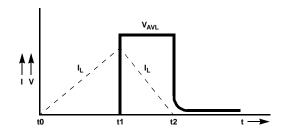
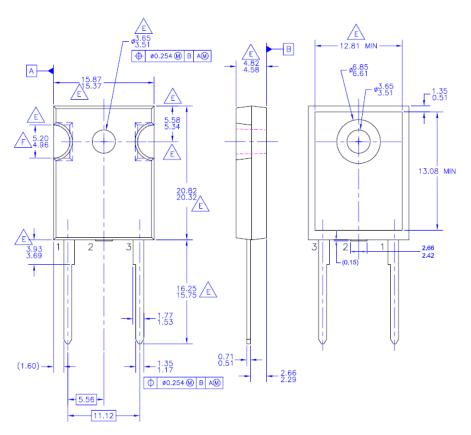


Figure 14. Avalanche Energy Test Circuit

Figure 15. Avalanche Current and Voltage Waveforms

### **Mechanical Dimensions**



NOTES: UNLESS OTHERWISE SPECIFIED

- A. PACKAGE REFERENCE: JEDEC TO-247, ISSUE E, VARIATION AB, DATED JUNE, 2004,
- DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
  ALL DIMENSIONS ARE IN MILLIMETERS.
- D. DRAWING CONFORMS TO ASME Y14.5 1994

E DOES NOT COMPLY JEDEC STANDARD VALUE

F. NOTCH MAY BE SQUARE

G. DRAWING FILENAME: MKT-TO247B02\_REV02

Figure 16. TO-247 2L - TO247, MOLDED, 2LD, JEDEC OPTION AB

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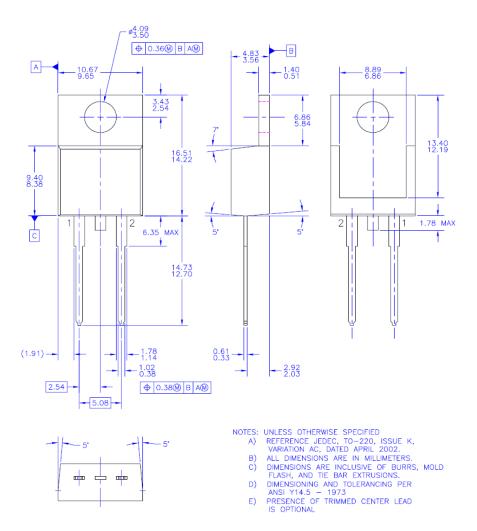


Figure 17. TO-220 2L - 2LD, TO220, JEDEC TO-220 VARIATION AC

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