

MMBF0202PLT1

Preferred Device

Power MOSFET 300 mAmps, 20 Volts

P-Channel SOT-23

These miniature surface mount MOSFETs low $R_{DS(on)}$ assure minimal power loss and conserve energy, making these devices ideal for use in small power management circuitry. Typical applications are dc-dc converters, power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

Features

- Low $R_{DS(on)}$ Provides Higher Efficiency and Extends Battery Life
- Miniature SOT-23 Surface Mount Package Saves Board Space
- Pb-Free Package is Available

MAXIMUM RATINGS ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Rating	Symbol	Value	Unit
Drain-to-Source Voltage	V_{DSS}	20	Vdc
Gate-to-Source Voltage - Continuous	V_{GS}	± 20	Vdc
Drain Current			mAdc
- Continuous @ $T_A = 25^\circ\text{C}$	I_D	300	
- Continuous @ $T_A = 70^\circ\text{C}$	I_D	240	
- Pulsed Drain Current ($t_p \leq 10 \mu\text{s}$)	I_{DM}	750	
Total Power Dissipation @ $T_A = 25^\circ\text{C}$ (Note 1)	P_D	225	mW
Operating and Storage Temperature Range	T_J, T_{stg}	-55 to 150	$^\circ\text{C}$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	625	$^\circ\text{C/W}$
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	T_L	260	$^\circ\text{C}$

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. Pulse Test: Pulse Width $\leq 300 \mu\text{s}$, Duty Cycle $\leq 2\%$.

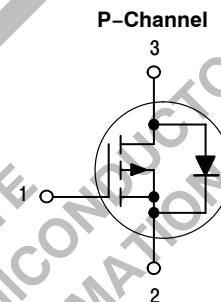


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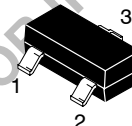
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300 mAMPS, 20 VOLTS

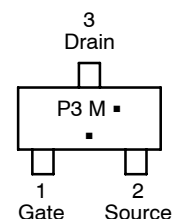
$R_{DS(on)} = 1.4 \Omega$



MARKING DIAGRAM AND PIN ASSIGNMENT



SOT-23
CASE 318
STYLE 21



P3 = Specific Device Code
M = Date Code*
▪ = Pb-Free Package

(Note: Microdot may be in either location)

*Date Code orientation and/or overbar may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping†
MMBF0202PLT1	SOT-23	3000 Tape & Reel
MMBF0202PLT1G	SOT-23 (Pb-Free)	3000 Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Preferred devices are recommended choices for future use and best overall value.

MMBF0202PLT1

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
OFF CHARACTERISTICS					
Drain-to-Source Breakdown Voltage (V _{GS} = 0 Vdc, I _D = 10 μA)	V _{(BR)DSS}	20	-	-	Vdc
Zero Gate Voltage Drain Current (V _{DS} = 16 Vdc, V _{GS} = 0 Vdc) (V _{DS} = 16 Vdc, V _{GS} = 0 Vdc, T _J = 125°C)	I _{DSS}	-	-	1.0 10	μAdc
Gate-Body Leakage Current (V _{GS} = ± 20 Vdc, V _{DS} = 0)	I _{GSS}	-	-	±100	nAdc
ON CHARACTERISTICS (Note 2)					
Gate Threshold Voltage (V _{DS} = V _{GS} , I _D = 250 μAdc)	V _{GS(th)}	1.0	1.7	2.4	Vdc
Static Drain-to-Source On-Resistance (V _{GS} = 10 Vdc, I _D = 200 mAdc) (V _{GS} = 4.5 Vdc, I _D = 50 mAdc)	r _{DS(on)}	-	0.9 2.0	1.4 3.5	Ω
Forward Transconductance (V _{DS} = 10 Vdc, I _D = 200 mAdc)	g _{FS}	-	600	-	mMhos
DYNAMIC CHARACTERISTICS					
Input Capacitance	(V _{DS} = 5.0 V)	C _{iss}	-	50	pF
Output Capacitance	(V _{DS} = 5.0 V)	C _{oss}	-	45	
Transfer Capacitance	(V _{DG} = 5.0 V)	C _{rss}	-	20	
SWITCHING CHARACTERISTICS (Note 3)					
Turn-On Delay Time	(V _{DD} = -15 Vdc, R _L = 75 Ω, I _D = 200 mAdc, V _{GEN} = -10 V, R _G = 6.0 Ω)	t _{d(on)}	-	2.5	ns
Rise Time		t _r	-	1.0	
Turn-Off Delay Time		t _{d(off)}	-	16	
Fall Time		t _f	-	8.0	
Gate Charge (See Figure 5)	(V _{DS} = 16 V, V _{GS} = 10 V, I _D = 200 mA)	Q _T	-	2700	pC
SOURCE-DRAIN DIODE CHARACTERISTICS					
Continuous Current	I _S	-	-	0.3	A
Pulsed Current	I _{SM}	-	-	0.75	
Forward Voltage (Note 3)	V _{SD}	-	1.5	-	V

- Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2%.
- Switching characteristics are independent of operating junction temperature.

TYPICAL ELECTRICAL CHARACTERISTICS

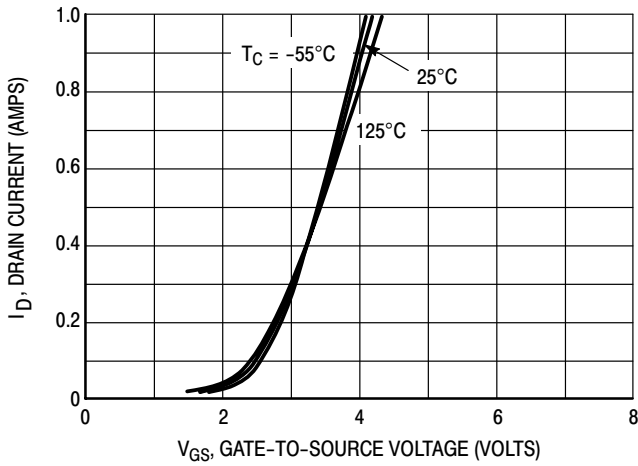


Figure 1. Transfer Characteristics

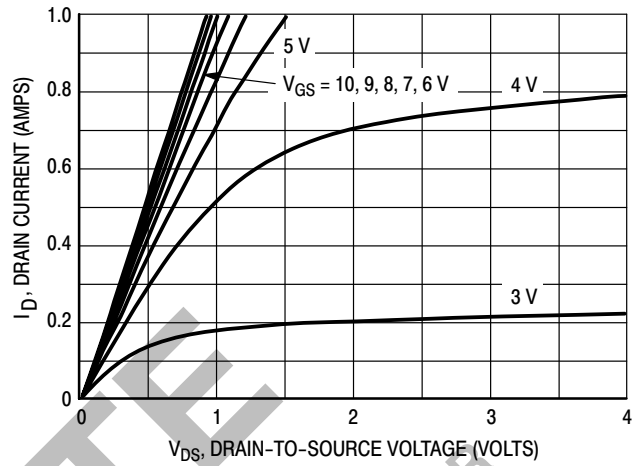


Figure 2. On-Region Characteristics

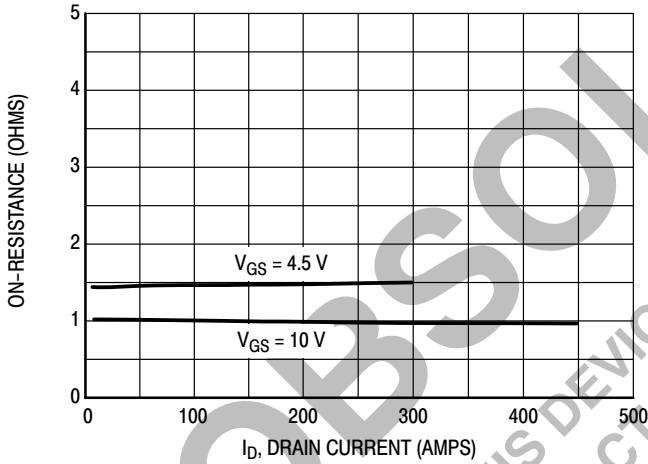


Figure 3. On-Resistance versus Drain Current

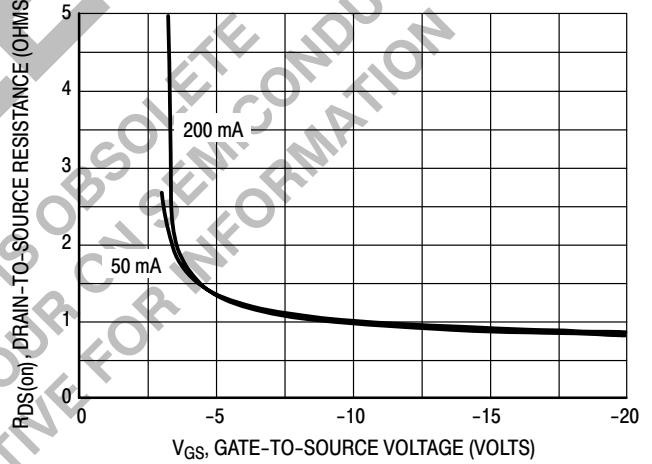


Figure 4. On-Resistance versus Gate-to-Source Voltage

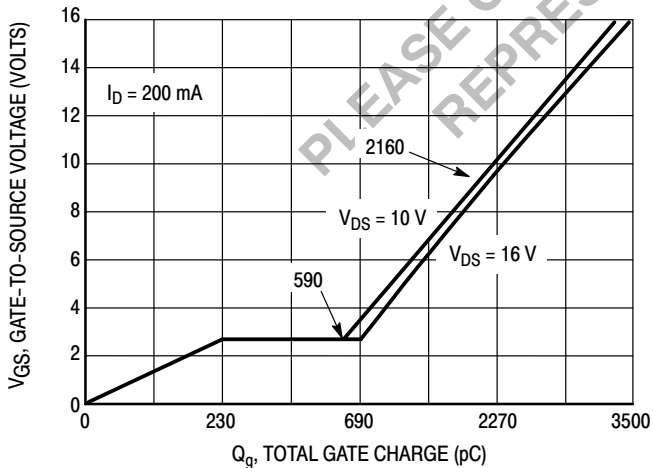


Figure 5. Gate Charge

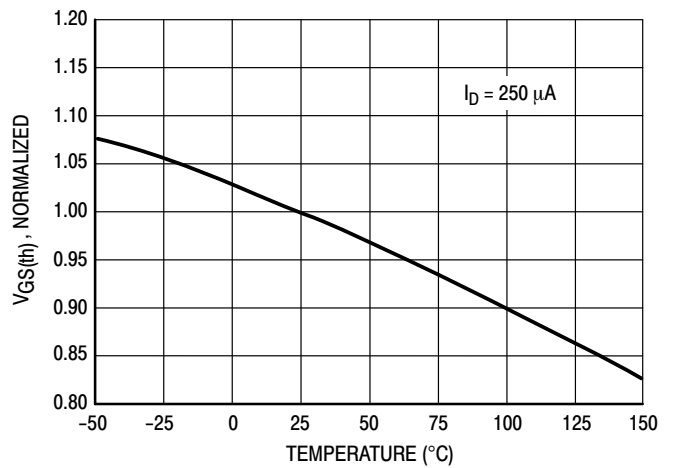


Figure 6. Threshold Voltage Variance Over Temperature

TYPICAL ELECTRICAL CHARACTERISTICS

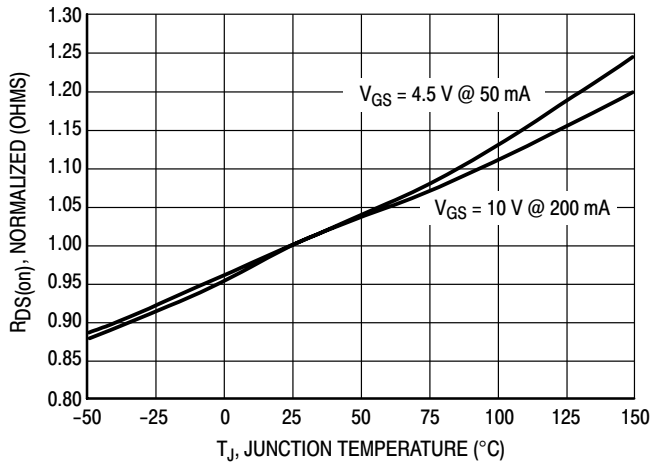


Figure 7. On-Resistance versus Junction Temperature

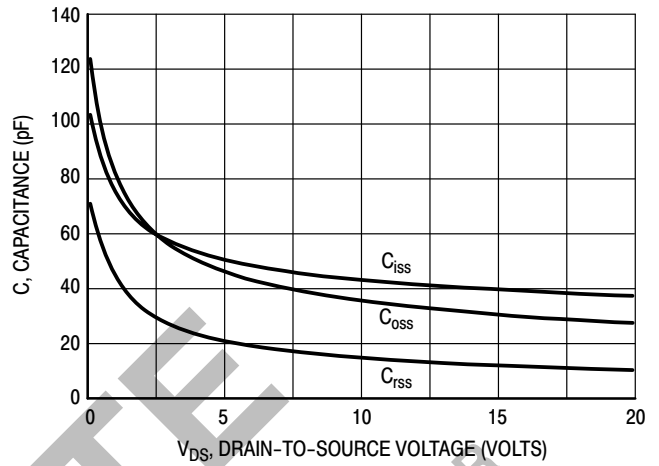


Figure 8. Capacitance

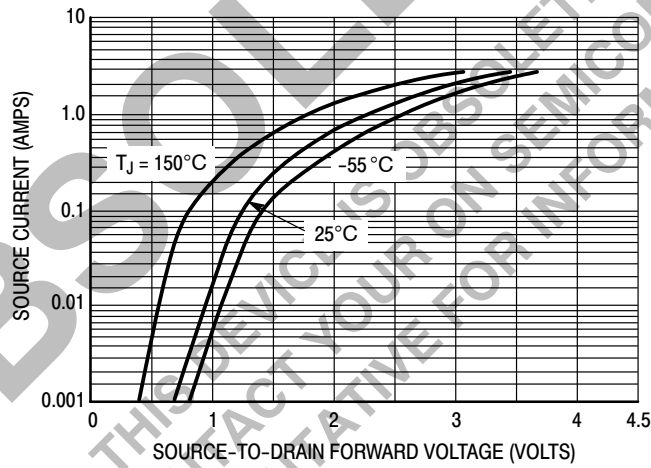
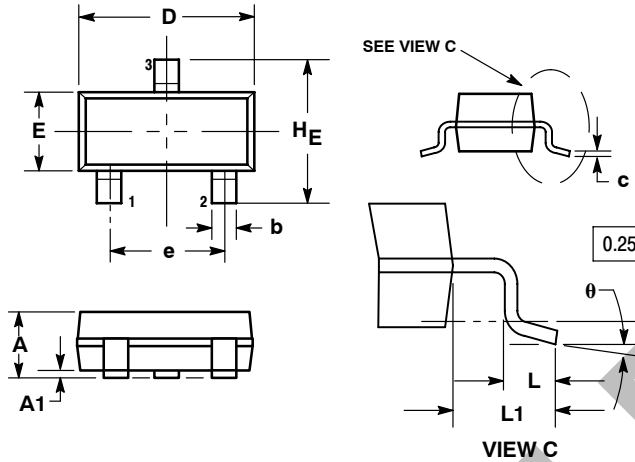


Figure 9. Source-to-Drain Forward Voltage versus Continuous Current (I_S)

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PACKAGE DIMENSIONS

SOT-23 (TO-236)
CASE 318-08
ISSUE AN

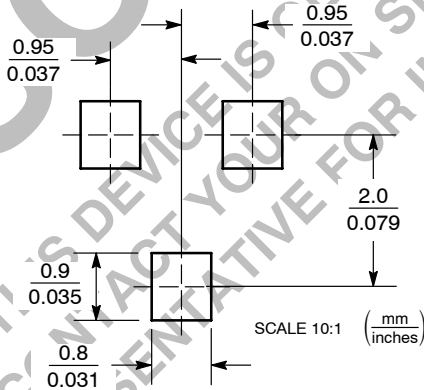


- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
 4. 318-01 THRU -07 AND -09 OBSOLETE, NEW STANDARD 318-08.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.89	1.00	1.11	0.035	0.040	0.044
A1	0.01	0.06	0.10	0.001	0.002	0.004
b	0.37	0.44	0.50	0.015	0.018	0.020
c	0.09	0.13	0.18	0.003	0.005	0.007
D	2.80	2.90	3.04	0.110	0.114	0.120
E	1.20	1.30	1.40	0.047	0.051	0.055
e	1.78	1.90	2.04	0.070	0.075	0.081
L	0.10	0.20	0.30	0.004	0.008	0.012
L1	0.35	0.54	0.69	0.014	0.021	0.029
HE	2.10	2.40	2.64	0.083	0.094	0.104

STYLE 21:
PIN 1. GATE
2. SOURCE
3. DRAIN

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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