NCV8440CWP, NCV8440ACWP

Protected Power MOSFET

2.6 A, 52 V, N-Channel, Logic Level, Clamped MOSFET w/ ESD Protection

MAXIMUM RATINGS ($T_J = 25^{\circ}C$ unless otherwise specified)

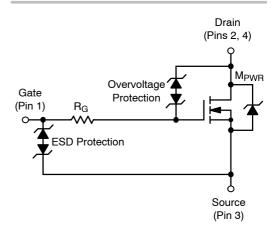
Rating	Symbol	Value	Unit
Drain-to-Source Voltage Internally Clamped	V _{DSS}	52-59	V
Gate-to-Source Voltage - Continuous	V _{GS}	±15	V
Operating and Storage Temperature Range	T _J , T _{stg}	-55 to 150	°C
Electro-Static Discharge Capability (HBM (MM		5000 500	V

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



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ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

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MOSFET ELECTRICAL CHARACTERISTICS (T, = 25°C unless otherwise specified) (Note 1)

Charac	teristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS		•				
Drain-to-Source Breakdown Voltage ($V_{GS} = 0 \text{ V}, I_D = 1.0 \text{ mA}, T_J = 25^{\circ}\text{C}$)		V _{(BR)DSS}	52	55	59	٧
Zero Gate Voltage Drain Current (V _{DS} = 40 V, V _{GS} = 0 V)		I _{DSS}			10	μΑ
Gate-Body Leakage Current $(V_{GS} = \pm 8 \text{ V}, V_{DS} = 0 \text{ V})$ $(V_{GS} = \pm 14 \text{ V}, V_{DS} = 0 \text{ V})$		I _{GSS}		±22	±10	μΑ
ON CHARACTERISTICS						
Gate Threshold Voltage $(V_{DS} = V_{GS}, I_D = 100 \mu A)$		V _{GS(th)}	1.3	1.75	2.5	V
Static Drain-to-Source On-Resistance ($V_{GS}=3.5$ V, $I_{D}=0.6$ A) ($V_{GS}=4.0$ V, $I_{D}=1.5$ A) ($V_{GS}=10$ V, $I_{D}=2.6$ A)		R _{DS(on)}		190 165 107	380 200 125	mΩ
SOURCE-DRAIN DIODE CHARACTERISTICS						
Forward On-Voltage	I _S = 2.6 A, V _{GS} = 0 V I _S = 2.6 A, V _{GS} = 0 V, T _J = 125°C	V _{SD}		0.81 0.66	1.5	V

^{1.} Wafers shipped as whole wafers.

ORDERING INFORMATION

Device	Shipping	
NCV8440CWP	Whole Wafers	
NCV8440ACWP	Whole Wafers	

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