N-Channel JFET -25 V, 20 to 40 mA, 40 mS, Dual

The NSVJ6904DSB6 is a composite type of JFET designed for compact size and high efficiency which can achieve high gain performance. This AEC–Q101 qualified and PPAP capable device is suited for automotive applications.

Features

- Large | yfs |
- Small Ciss
- Ultralow Noise Figure
- CPH6 Package is Pin-Compatible with SC-74
- AEC-Q101 Qualified and PPAP Capable
- Mounting Area is Greatly Reduced by Incorporating Two JFETs of the NSVJ3910SB3 in One Package of CPH6 Compared with Using Two Separate Packages

Typical Applications

- AM Tuner RF Amplification
- Low Noise Amplifier

Specifications

ABSOLUTE MAXIMUM RATINGS ($T_a = 25^{\circ}C$)

_	Symbo		
Parameter	I	Value	Unit
Drain to Source Voltage	V _{DSX}	25	V
Gate to Drain Voltage	V_{GDS}	-25	V
Gate Current	I _G	10	mA
Drain Current	I _D	50	mA
Allowable Power Dissipation 1 unit	PD	400	mW
Total Power Dissipation	PT	700	mW
Operating Junction and Storage Temperature	$T_{J,}T_{Stg}$	-55 to +150	°C

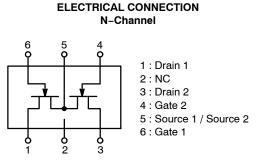
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

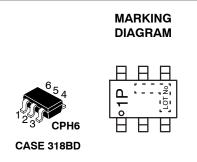


ON Semiconductor®

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V _{DSS}	R _{DS} (on) MAX	I _D MAX
60 V	78 m Ω @ 10 V	4 5 4
00 V	104 mΩ @ 4.5 V	4.5 A





ORDERING INFORMATION

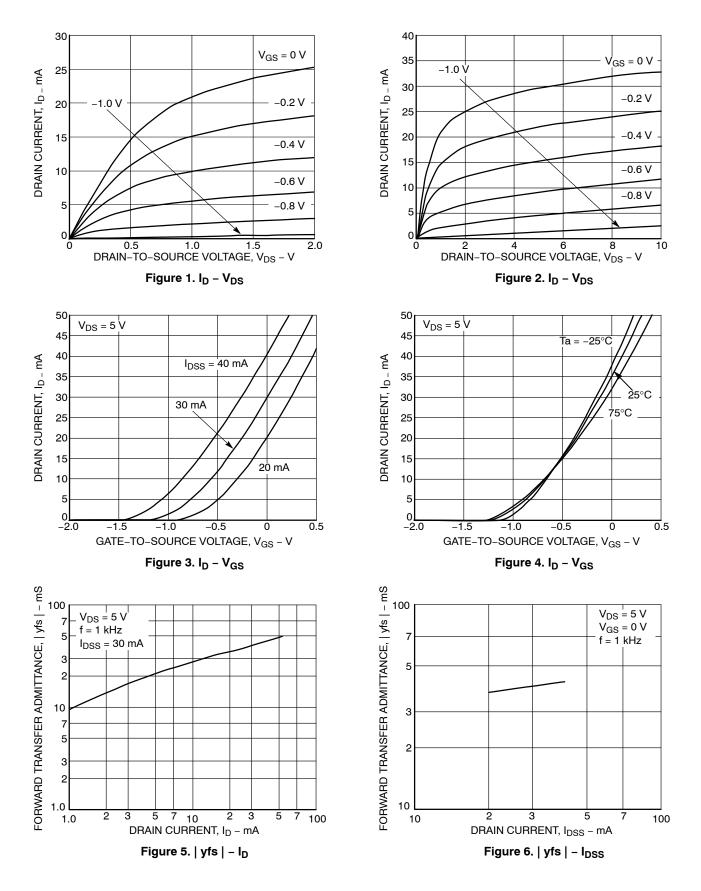
See detailed ordering, marking and shipping information in the package dimensions section on page 4 of this data sheet.

ELECTRICAL CHARACTERISTICS (T_J = 25°C, (Note 1))

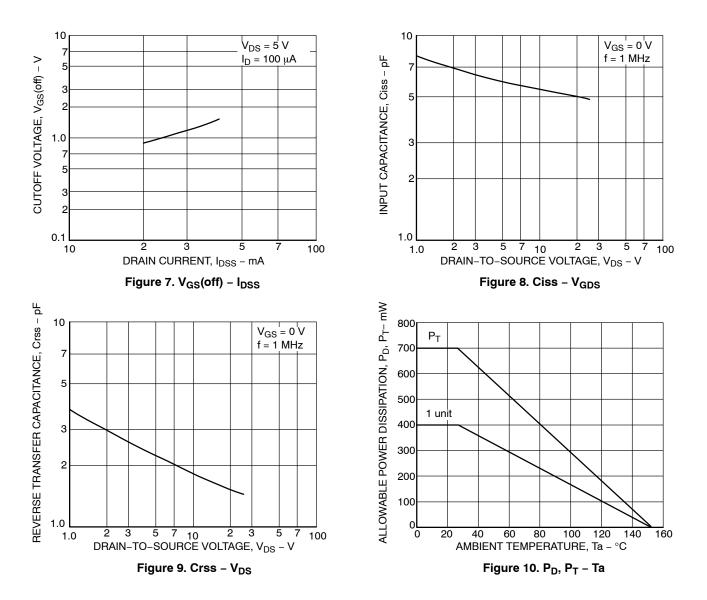
Characteristic	Symbol	Conditions	Min	Тур	Max	Unit
Gate to Drain Breakdown Voltage	V _{(BR)GDS}	$I_G = -10 \ \mu A$, $V_{DS} = 0 \ V$	-25	-	-	V
Gate to Source Leakage Current	I _{GSS}	$V_{GS} = -10 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$	-	-	-1.0	nA
Cutoff Voltage	V _{GS(off)}	V_{DS} = 5 V, I_D = 100 μ A	-0.6	-1.2	-1.8	V
Zero-Gate Voltage Drain Current	I _{DSS}	V_{DS} = 5 V, V_{GS} = 0 V	20	-	40	mA
Forward Transfer Admittance	yfs	V_{DS} = 5 V, V_{GS} = 0 V, f = 1 kHz	30	40	-	mS
Input Capacitance	Ciss	V_{DS} = 5 V, V_{GS} = 0 V, f = 1 MHz	-	6.0	-	pF
Reverse Transfer Capacitance	Crss		-	2.3	-	pF
Noise Figure	NF	V_{DS} = 5 V, V_{GS} = 0 V, f = 100 MHz	-	2.1	2.8	dB

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 1. The specifications shown above are for each individual JFET.

CHARACTERISTICS



CHARACTERISTICS



ORDERING INFORMATION

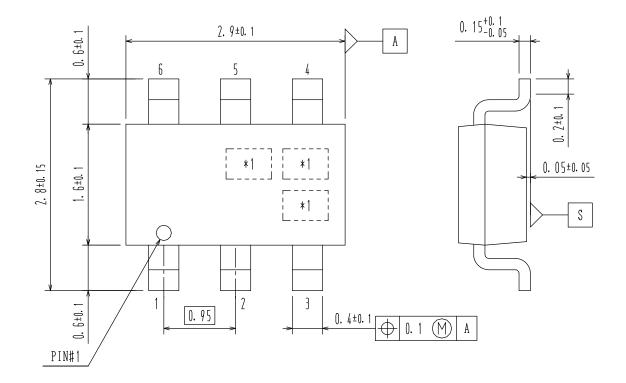
Device Order Number	Specific Device Marking	Package Type	Shipping [†]
NSVJ6904DSB6T1G	1P	CPH6 (Pb-Free / Halogen Free)	3,000 / Tape & Reel

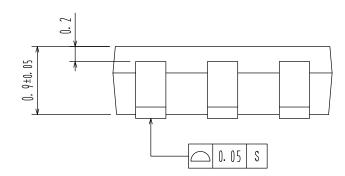
+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.



CPH6 CASE 318BD ISSUE O

DATE 30 NOV 2011





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PAGE 2 OF 2

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