MOSFET – Power, N-Channel, SUPERFET III, FRFET

650 V, 24 A, 150 mΩ

Description

SUPERFET III MOSFET is ON Semiconductor's brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This advanced technology is tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate.

Consequently, SUPERFET III MOSFET is very suitable for the various power system for miniaturization and higher efficiency.

SUPERFET III FRFET MOSFET's optimized reverse recovery performance of body diode can remove additional component and improve system reliability.

Features

- $700 \text{ V} @ \text{T}_{\text{I}} = 150^{\circ}\text{C}$
- Typ. $R_{DS(on)} = 121 \text{ m}\Omega$
- Ultra Low Gate Charge (Typ. Q_g = 43 nC)
- Low Effective Output Capacitance (Typ. Coss(eff.) = 400 pF)
- 100% Avalanche Tested
- These Devices are Pb-Free and are RoHS Compliant

Applications

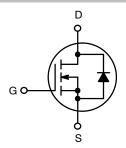
- Telecom / Server Power Supplies
- Industrial Power Supplies
- EV Charger
- UPS / Solar



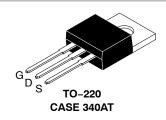
ON Semiconductor®

www.onsemi.com

V _{DSS}	R _{DS(ON)} MAX I _D MA	
650 V	150 mΩ @ 10 V	24 A



POWER MOSFET



MARKING DIAGRAM



\$Y = ON Semiconductor Logo &Z = Assembly Plant Code &3 = Data Code (Year & Week)

&K = Lot

NTP150N65S3HF = Specific Device Code

ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

ABSOLUTE MAXIMUM RATINGS ($T_C = 25^{\circ}C$, Unless otherwise noted)

Symbol	Symbol Parameter		Value	Unit	
V_{DSS}	Drain to Source Voltage	ee Voltage 650		V	
V_{GSS}	Gate to Source Voltage	- DC	±30	V	
		- AC (f > 1 Hz)	±30		
I _D	Drain Current	– Continuous (T _C = 25°C)	24	Α	
		- Continuous (T _C = 100°C)	15.2		
I _{DM}	Drain Current	- Pulsed (Note 1)	60	Α	
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		275	mJ	
I _{AS}	Avalanche Current (Note 2) Repetitive Avalanche Energy (Note 1)		3.8	Α	
E _{AR}			1.92	mJ	
dv/dt	MOSFET dv/dt		100	V/ns	
	Peak Diode Recovery dv/dt (Note 3)	50			
P_{D}	Power Dissipation	(T _C = 25°C)	192	W	
		- Derate Above 25°C	1.54	W/°C	
T _J , T _{STG}	Operating and Storage Temperature Range	•	-55 to +150	°C	
TL	Maximum Lead Temperature for Soldering, 1/	8" from Case for 5 seconds	300	°C	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Repetitive rating: pulse–width limited by maximum junction temperature.

2. $I_{AS} = 3.8 \text{ A}$, $R_{G} = 25 \Omega$, starting $T_{J} = 25^{\circ}\text{C}$.

3. $I_{SD} \le 12 \text{ A}$, $di/dt \le 200 \text{ A/µs}$, $V_{DD} \le 400 \text{ V}$, starting $T_{J} = 25^{\circ}\text{C}$.

THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
$R_{ heta JC}$	Thermal Resistance, Junction to Case, Max.	0.65	°C/W
$R_{ heta JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	

PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Marking	Package	Packing Method	Reel Size	Tape Width	Quantity
NTP150N65S3HF	NTP150N65S3HF	TO-220	Tube	N/A	N/A	50 Units

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
OFF CHARACT	ERISTICS		•	•		
BV _{DSS}	Drain to Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 1 \text{ mA}, T_J = 25^{\circ}\text{C}$	650			V
		V _{GS} = 0 V, I _D = 1 mA, T _J = 150°C	700			V
$\Delta BV_{DSS}/\Delta T_{J}$	Breakdown Voltage Temperature Coefficient	I _D = 15 mA, Referenced to 25°C		0.62		V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 650 V, V _{GS} = 0 V			10	μΑ
		V _{DS} = 520 V, T _C = 125°C		67		1
I _{GSS}	Gate to Body Leakage Current	V _{GS} = ±30 V, V _{DS} = 0 V			±100	nA
ON CHARACTE	RISTICS					
V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 0.54 \text{ mA}$	3.0		5.0	V
R _{DS(on)}	Static Drain to Source On Resistance	V _{GS} = 10 V, I _D = 12 A		121	150	mΩ
9FS	Forward Transconductance	V _{DS} = 20 V, I _D = 12 A		14		S
OYNAMIC CHA	RACTERISTICS		•	•		
C _{iss}	Input Capacitance	V _{DS} = 400 V, V _{GS} = 0 V, f = 1 MHz		1985		pF
C _{oss}	Output Capacitance	1		40		pF
C _{oss(eff.)}	Effective Output Capacitance	V _{DS} = 0 V to 400 V, V _{GS} = 0 V		400		pF
C _{oss(er.)}	Energy Related Output Capacitance	V _{DS} = 0 V to 400 V, V _{GS} = 0 V		71		pF
Q _{g(tot)}	Total Gate Charge at 10 V	V _{DS} = 400 V, I _D = 12 A, V _{GS} = 10 V (Note 4)		43		nC
Q _{gs}	Gate to Source Gate Charge			13		nC
Q _{gd}	Gate to Drain "Miller" Charge			17		nC
ESR	Equivalent Series Resistance	f = 1 MHz		5.0		Ω
WITCHING CH	HARACTERISTICS					
t _{d(on)}	Turn-On Delay Time	$V_{DD} = 400 \text{ V}, I_D = 12 \text{ A}, V_{GS} = 10 \text{ V}$		21		ns
t _r	Turn-On Rise Time	$R_g = 4.7 \Omega$ (Note 4)		19		ns
t _{d(off)}	Turn-Off Delay Time			63		ns
t _f	Turn-Off Fall Time			14		ns
SOURCE-DRAI	N DIODE CHARACTERISTICS		•	•		
I _S	Maximum Continuous Source to Drain Diode Forward Current				24	Α
I _{SM}	Maximum Pulsed Source to Drain Diode Forward Current				60	Α
V _{SD}	Source to Drain Diode Forward Voltage	V _{DD} = 400 V, I _{SD} = 12 A			1.3	V
			1	1	l	
t _{rr}	Reverse Recovery Time	V _{DD} = 400 V, I _{SD} = 12 A, dI _F /dt = 100 A/μs		88		ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially independent of operating temperature typical characteristics.

TYPICAL CHARACTERISTICS

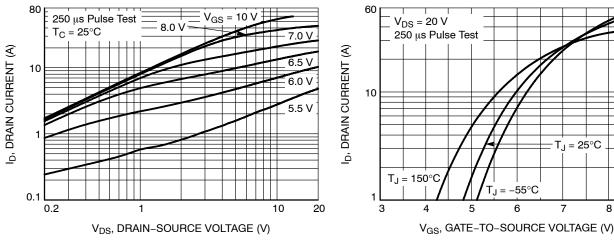


Figure 1. On-Region Characteristics



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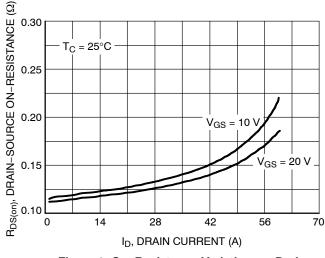


Figure 3. On-Resistance Variation vs. Drain **Current and Gate Voltage**

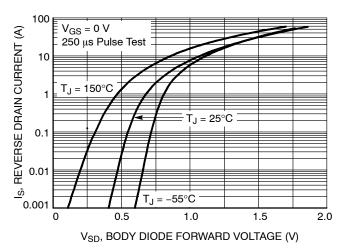


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

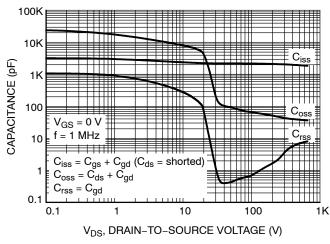


Figure 5. Capacitance Characteristics

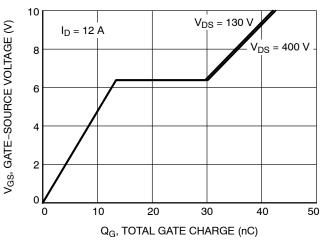


Figure 6. Gate Charge Characteristics

TYPICAL CHARACTERISTICS

3.0

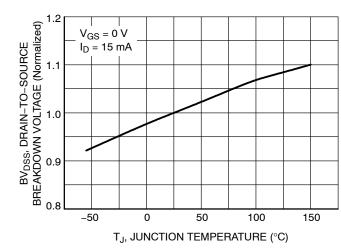
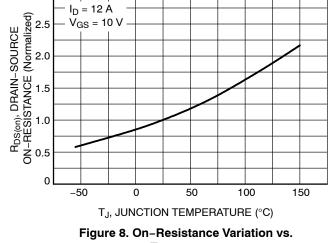


Figure 7. Breakdown Voltage Variation vs. Temperature



Temperature

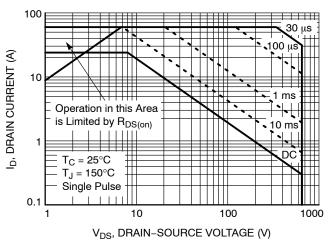


Figure 9. Maximum Safe Operating Area

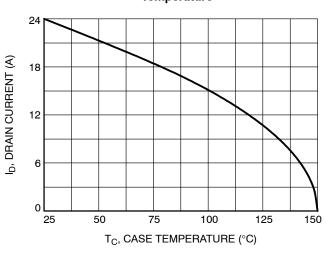


Figure 10. Maximum Drain Current vs. Case **Temperature**

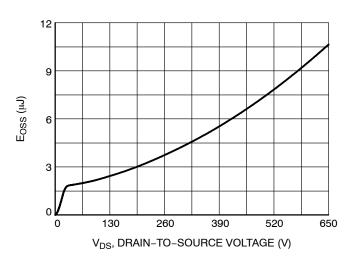


Figure 11. E_{OSS} vs. Drain-to-Source Voltage

TYPICAL CHARACTERISTICS

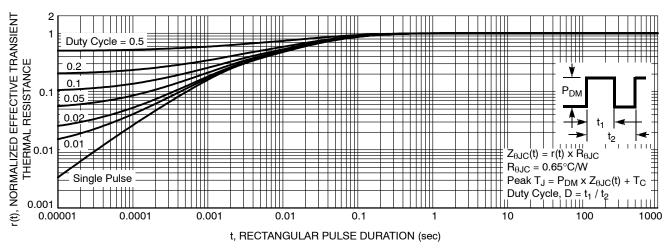


Figure 12. Transient Thermal Response Curve

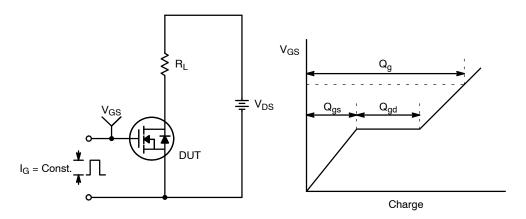


Figure 13. Gate Charge Test Circuit & Waveform

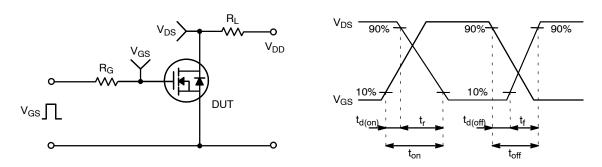


Figure 14. Resistive Switching Test Circuit & Waveforms

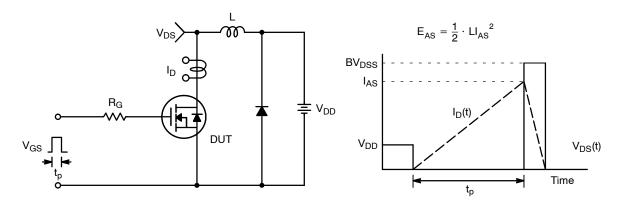


Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms

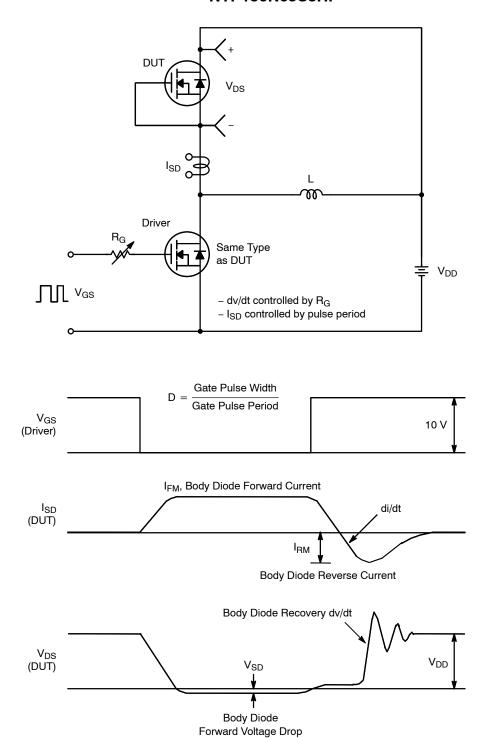
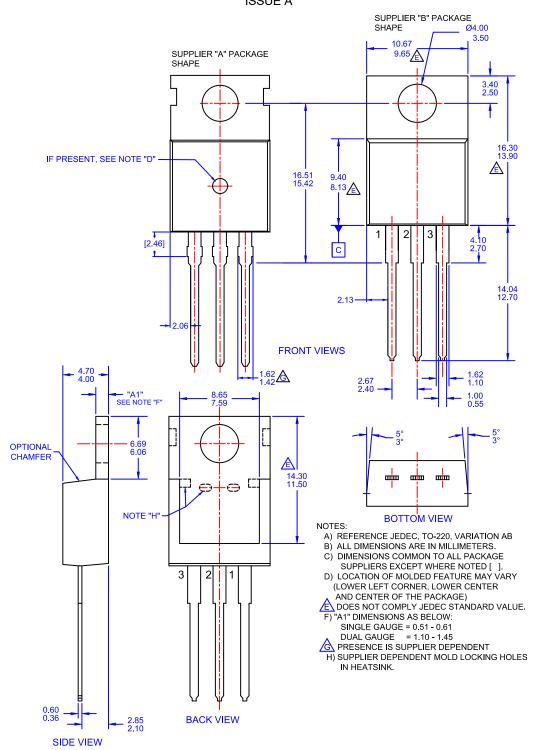


Figure 16. Peak Diode Recovery dv/dt Test Circuit & Waveforms

PACKAGE DIMENSIONS

TO-220-3LD CASE 340AT ISSUE A



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