<u>MOSFET</u> – Power, N-Channel, SUPERFET[®] III

Product Preview NTP360N80S3Z 800 V, 360 mΩ, 13 A

Description

800 V SUPERFET III is ON Semiconductor's high performance MOSFET family offering 800 V breakdown voltage.

New 800 V SUPERFET III MOSFET which is optimized for primary switch of flyback converter, enables lower switching losses and case temperature without sacrificing EMI performance due to its optimized design.

This new family of 800 V SUPERFET III MOSFET enables to make more efficient, compact, cooler and more robust applications because of its remarkable performance in switching power applications such as Laptop adapter, Audio, Lighting, ATX power and industrial power supplies.

Features

- Typ. $R_{DS(on)} = 300 \text{ m}\Omega$
- Ultra Low Gate Charge (Typ. $Q_g = 24.7 \text{ nC}$)
- Low Stored Energy in Output Capacitance (Eoss = 2.9 µJ @ 400 V)
- 100% Avalanche Tested
- ESD Improved Capability with Zener Diode
- RoHS Compliant

Applications

- Adapters / Chargers
- LED Lighting
- AUX Power
- Audio
- Industrial Power

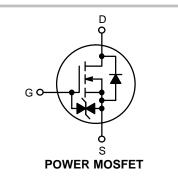
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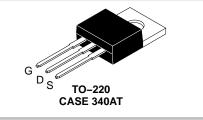


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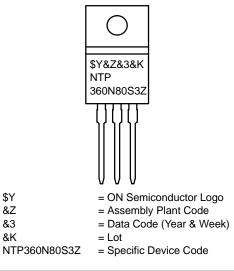
www.onsemi.com

V _{(BR)DSS}	R _{DS(ON)} MAX	I _D MAX
800 V	$360 \text{ m}\Omega$	13 A





MARKING DIAGRAM



ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

Symbol	Para	Value	Unit		
V _{DSS}	Drain-to-Source Voltage	800	V		
V _{GS}	Gate-to-Source Voltage	DC	±20	V	
		AC (f > 1 Hz)	±30		
ID	Drain Current	Continuous ($T_C = 25^{\circ}C$)	13	А	
		Continuous ($T_c = 100^{\circ}C$)	8.2		
I _{DM}	Drain Current	rent Pulsed (Note 1)		А	
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		TBD	mJ	
I _{AS}	Avalanche Current (Note 2)		TBD	А	
E _{AR}	Repetitive Avalanche Energy (Note 1)		TBD	mJ	
dv/dt	MOSFET dv/dt		100	V/ns	
	Peak Diode Recovery dv/dt (Note 3)		20		
P _D	Power Dissipation	(T _C = 25°C)	152	W/∘C	
		Derate Above 25°C	TBD		
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150	°C	
ΤL	Lead Temperature Soldering Reflow for Soldering Purposes (1/8" from Case for 10 seconds)		260	°C	

ABSOLUTE MAXIMUM RATINGS (T_J = 25°C, unless otherwise noted)

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected. 1. Repetitive rating: pulse-width limited by maximum junction temperature. 2. $I_{AS} = TBD A$, $R_G = 25 \Omega$, starting $T_J = 25^{\circ}C$. 3. $I_{SD} \le 6 A$, di/dt $\le 200 A/\mu s$, $V_{DD} \le 400 V$, starting $T_J = 25^{\circ}C$.

THERMAL RESISTANCE RATINGS

Symbol	Parameter	Value	Unit
$R_{\theta JC}$	Junction-to-Case - Steady State	TBD	°C/W
R_{\thetaJA}	Junction-to-Ambient - Steady State	TBD	

PACKAGE MARKING AND ORDERING INFORMATION

Part Num	ber	Top Marking	Package	Packing Method	Reel Size	Tape Width	Quantity
NTP360N8	0S3Z	NTP360N80S3Z	TO-220	Tube	N/A	N/A	50 Units

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted)

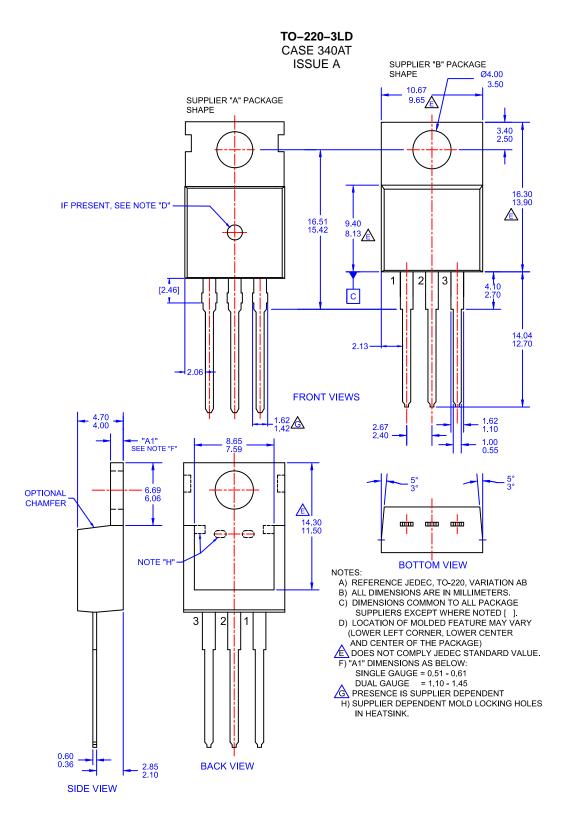
Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
OFF CHARACT	ERISTICS	•		•		
BV _{DSS}	Drain-to-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = 1 \text{ mA}, \text{ T}_{J} = 25^{\circ}\text{C}$	800			V
		$V_{GS} = 0 \text{ V}, \text{ I}_{D} = 1 \text{ mA}, \text{ T}_{J} = 150^{\circ}\text{C}$	900			V
$\Delta \text{BV}_{\text{DSS}} / \Delta \text{T}_{\text{J}}$	Breakdown Voltage Temperature Coefficient	I_D = 1 mA, Referenced to 25°C		0.96		V/°C
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 800 \text{ V}, V_{GS} = 0 \text{ V}$			10	μΑ
		V_{DS} = 640 V, T_{C} = 125°C		20		1
I _{GSS}	Gate-to-Body Leakage Current	V_{GS} = ±20 V, V_{DS} = 0 V			10	μΑ
ON CHARACTE	ERISTICS	•				
V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 0.3 \text{ mA}$	2.2		3.8	V
R _{DS(on)}	Static Drain-to-Source On Resistance	V _{GS} = 10 V, I _D = 6.5 A		300	360	mΩ
9 _{FS}	Forward Transconductance	V _{DS} = 20 V, I _D = 6.5 A		13		S
DYNAMIC CHA	RACTERISTICS			•		
C _{iss}	Input Capacitance	V_{DS} = 400 V, V_{GS} = 0 V, f = 250 kHz		1120		pF
C _{oss}	Output Capacitance	1 1		16.4		pF
Coss(eff.)	Effective Output Capacitance	$V_{DS} = 0 V$ to 400 V, $V_{GS} = 0 V$		315		pF
C _{oss(er.)}	Energy Related Output Capacitance	V_{DS} = 0 V to 400 V, V_{GS} = 0 V		34		pF
Q _{g(tot)}	Total Gate Charge at 10 V	$V_{DS} = 400 \text{ V}, \text{ I}_{D} = 6.5 \text{ A}, \text{ V}_{GS} = 10 \text{ V}$ (Note 4)		24.7		nC
Q _{gs}	Gate-to-Source Gate Charge			6		nC
Q _{gd}	Gate-to-Drain "Miller" Charge			10.2		nC
ESR	Equivalent Series Resistance	f = 1 MHz		3.6		Ω
SWITCHING CH	HARACTERISTICS	•		•		
t _{d(on)}	Turn-On Delay Time	V_{DD} = 400 V, I _D = 6.5 A, V _{GS} = 10 V,		20.3		ns
t _r	Turn-On Rise Time	$R_g = 25 \Omega$ (Note 4)		2.8		ns
t _{d(off)}	Turn-Off Delay Time			37		ns
t _f	Turn-Off Fall Time			10.1		ns
SOURCE-DRAI	N DIODE CHARACTERISTICS	•		•		-
I _S	Maximum Continuous Source-to-Drain Diode Forward Current				13	Α
I _{SM}	Maximum Pulsed Source-to-Drain Diode Forward Current				32.5	Α
V _{SD}	Source-to-Drain Diode Forward Voltage	$V_{GS} = 0 V, I_{SD} = 6.5 A$			1.2	V
t _{rr}	Reverse Recovery Time	$V_{GS} = 0 V, I_{SD} = 6.5 A,$		370		ns
Q _{rr}	Reverse Recovery Charge	dI _F /dt = 100 A/µs		3.2		μC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially independent of operating temperature typical characteristics.

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PUBLICATION ORDERING INFORMATION

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