## **NTP5N60**

**Preferred Devices** 

# Product Preview

# Power MOSFET 5 Amps, 600 Volts

N-Channel TO-220

Designed for high voltage, high speed switching applications in power supplies, converters, power motor controls and bridge circuits.

#### **Features**

- Higher Current Rating
- Lower R<sub>DS(on)</sub>
- Lower Capacitances
- Lower Total Gate Charge
- Tighter V<sub>SD</sub> Specifications
- Avalanche Energy Specified

#### **Typical Applications**

- Switch Mode Power Supplies
- PWM Motor Controls
- Converters
- Bridge Circuits

#### MAXIMUM RATINGS (T<sub>C</sub> = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DSS</sub>	600	Vdc
Drain-Gate Voltage (R <sub>GS</sub> = 1.0 MΩ)	$V_{DGR}$	600	Vdc
Gate-Source Voltage - Continuous - Non-Repetitive (t <sub>p</sub> ≤10 ms)	V <sub>GS</sub> V <sub>GS</sub>	±20 ±40	Vdc
Drain – Continuous @ T <sub>A</sub> 25°C – Continuous @ T <sub>A</sub> 100°C – Single Pulse (t <sub>p</sub> ≤10 μs)	I <sub>D</sub> I <sub>D</sub> I <sub>DM</sub>	5 3.8 17.5	Adc Apk
Total Power Dissipation @ T <sub>A</sub> 25°C Derate above 25°C Total Power Dissipation @ T <sub>A</sub> 25°C (Note 1.)	PD	96 0,77 1.75	Watts W/°C Watts
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C
Single Drain-to-Source Avalanche Energy – Starting $T_J = 25^{\circ}C$ ( $V_{DD} = 100 \text{ V}, V_{GS} = 10 \text{ Vdc},$ $I_L(pk) = 5 \text{ A}, L = 10 \text{ mH}, V_{DS} = 600 \text{ Vdc}, R_G = 25 \Omega)$	E <sub>AS</sub>	80	mJ
Thermal Resistance  – Junction–to–Case  – Junction–to–Ambient	R <sub>θJC</sub> R <sub>θJA</sub>	1.3 62.5	°C/W
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	TL	260	°C

Repetitive rating; pulse width limited by maximum junction temperature.

This document contains information on a product under development. ON Semiconductor reserves the right to change or discontinue this product without notice.

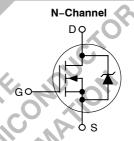


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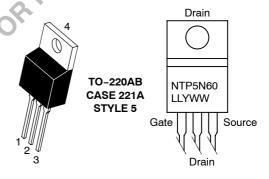
http://onsemi.com

## 5 AMPERES 600 VOLTS

 $R_{DS(on)} = 2400 \text{ m}\Omega$ 



### MARKING DIAGRAMS AND PIN ASSIGNMENTS



 NTP5N60
 = Device Code

 LL
 = Location Code

 Y
 = Year

 WW
 = Work Week

#### **ORDERING INFORMATION**

Device	Package	Shipping
NTP5N60	TO-220AB	50 Units/Rail

**Preferred** devices are recommended choices for future use and best overall value.

## **NTP5N60**

# **ELECTRICAL CHARACTERISTICS** ( $T_C = 25^{\circ}C$ unless otherwise noted)

	haracteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS			1			
Drain-to-Source Breakdown V (V <sub>GS</sub> = 0 Vdc, I <sub>D</sub> = 250 μAdc Temperature Coefficient (Pos	V <sub>(BR)DSS</sub>	600 -	- 700	_ _	Vdc mV/°C	
Zero Gate Voltage Drain Current (V <sub>DS</sub> = 600 Vdc, V <sub>GS</sub> = 0 Vdc) (V <sub>DS</sub> = 600 Vdc, V <sub>GS</sub> = 0 Vdc, T <sub>J</sub> =125°C)		I <sub>DSS</sub>	- -	- -	10 100	μAdc
Gate-Body Leakage Current (\	I <sub>GSS</sub>	-	-	±100	nAdc	
ON CHARACTERISTICS (Note	1)					
Gate Threshold Voltage $(V_{DS} = V_{GS}, I_D = 250 \mu Adc)$ Temperature Coefficient (Ne $(V_{DS} = V_{GS}, V_{DS} = V_{DS})$	V <sub>GS(th)</sub>	2.0	2.7 6.0	4.0	Vdc mV/°C	
Static Drain-to-Source On-Re	sistance (V <sub>GS</sub> = 10 Vdc, I <sub>D</sub> = 2.5 Adc)	R <sub>DS(on)</sub>	-	2100	2400	mOhm
Static Drain-to-Source On-Re $(V_{GS} = 10 \text{ Vdc}, I_D = 5 \text{ Adc})$ $(V_{GS} = 10 \text{ Vdc}, I_D = 2.5 \text{ Adc},$		V <sub>DS(on)</sub>	- -	- -	14.4 13.1	V
Forward Transconductance (V	<sub>DS</sub> = 15 Vdc, I <sub>D</sub> = 2.5 Adc)	9 <sub>FS</sub>	0.7	3.8	=	mhos
DYNAMIC CHARACTERISTICS			4, .	0	4	
Input Capacitance		C <sub>iss</sub>		540	780	pF
Output Capacitance	$(V_{DS} = 25 \text{ Vdc}, V_{GS} = 0 \text{ Vdc}, f = 1.0 \text{ MHz})$	C <sub>oss</sub>	·G	125	180	
Transfer Capacitance		C <sub>rss</sub>	-	8.0	20	
SWITCHING CHARACTERISTIC	CS (Note 2)	8				
Turn-On Delay Time		t <sub>d(on)</sub>		12	20	ns
Rise Time	$(V_{DD} = 300 \text{ Vdc}, I_D = 5 \text{ Adc}, V_{GS} = 10 \text{ Vdc},$	$t_{\rm r}$	_	7.0	10	
Turn-Off Delay Time	$R_G = 9.1 \Omega$	t <sub>d(off)</sub>	-	19	40	
Fall Time		t <sub>f</sub>	-	10	20	
Gate Charge		Q <sub>T</sub>	-	5.0	10	nC
	$(V_{DS} = 400 \text{ Vdc}, I_{D} = 5 \text{ Adc}, V_{GS} = 10 \text{ Vdc})$	Q <sub>1</sub>	-	2.7	-	
		Q <sub>2</sub>	-	2.0	-	
SOURCE-DRAIN DIODE CHAR	ACTERISTICS					
Forward On-Voltage (Note 1)	$(I_S = 5 \text{ Adc}, V_{GS} = 0 \text{ Vdc})$ $(I_S = 5 \text{ Adc}, V_{GS} = 0 \text{ Vdc}, T_J = 125^{\circ}\text{C})$	V <sub>SD</sub>	- -	0.86 0.75	1.0	Vdc
Reverse Recovery Time		t <sub>rr</sub>	-	655	-	ns
	( C No V 2 2 V 1	ta	-	103	-	
	$(I_S = 5 \text{ Adc}, V_{GS} = 0 \text{ Vdc}, \\ di_S/dt = 100 \text{ A}/\mu \text{s})$	t <sub>b</sub>	-	552	-	]
Reverse Recovery Stored Charge		Q <sub>RR</sub>	-	1.9	_	μС
Dulas Tast, Dulas Mishla Moo	Duty Cycle 100/					

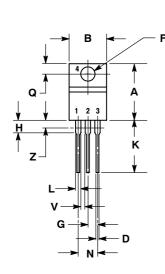
Pulse Test: Pulse Width ≤300 μs, Duty Cycle ≤ 2%.
 Switching characteristics are independent of operating junction temperature.

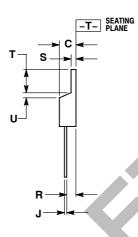
#### NTP5N60

#### PACKAGE DIMENSIONS

#### **TO-220 THREE-LEAD** TO-220AB

CASE 221A-09 **ISSUE AA** 





#### NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: INCH.
  DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.570	0.620	14.48	15.75
B	0.380	0.405	9.66	10.28
C	0.160	0.190	4.07	4.82
٥	0.025	0.035	0.64	0.88
F	0.142	0.147	3.61	3.73
G	0.095	0.105	2.42	2.66
Н	0.110	0.155	2.80	3.93
7	0.018	0.025	0.46	0.64
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.39
T	0.235	0.255	5.97	6.47
=	0.000	0.050	0.00	1.27
٧	0.045		1.15	
Z		0.080		2.04

Q	0.1		
R	0.08	S	0.045
T	0.235		
U	0.000		
V	0.045		
Z	D	C	
STYLE 5: PIN 1. GATE			
2. PRAIN			
3. SQURCE			
4. DRAIN			
4. DRAIN			
5. DRAIN			
6. DRAIN			
7. DRAIN			
7. DRAIN			
8. DRAIN			
9. DRAIN			
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