MOSFET - Power, N-Channel, SUPERFET® III

Product Preview

NTPF360N80S3Z

800 V, 360 mΩ, 13 A

Description

800 V SUPERFET III is ON Semiconductor's high performance MOSFET family offering 800 V breakdown voltage.

New 800 V SUPERFET III MOSFET which is optimized for primary switch of flyback converter, enables lower switching losses and case temperature without sacrificing EMI performance due to its optimized design.

This new family of 800 V SUPERFET III MOSFET enables to make more efficient, compact, cooler and more robust applications because of its remarkable performance in switching power applications such as Laptop adapter, Audio, Lighting, ATX power and industrial power supplies.

Features

- Typ. $R_{DS(on)} = 300 \text{ m}\Omega$
- Ultra Low Gate Charge (Typ. Q_g = 24.7 nC)
- Low Stored Energy in Output Capacitance (Eoss = 2.9 μJ @ 400 V)
- 100% Avalanche Tested
- ESD Improved Capability with Zener Diode
- RoHS Compliant

Applications

- Adapters / Chargers
- LED Lighting
- AUX Power
- Audio
- Industrial Power

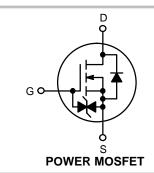
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ON Semiconductor®

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V _{(BR)DSS}	R _{DS(ON)} MAX	I _D MAX		
800 V	360 m $Ω$	13 A		





MARKING DIAGRAM



\$Y = ON Semiconductor Logo &Z = Assembly Plant Code &3 = Data Code (Year & Week)

= Lot

NTPF360N80S3Z = Specific Device Code

ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet

ABSOLUTE MAXIMUM RATINGS ($T_J = 25^{\circ}C$, unless otherwise noted)

Symbol	Parameter		Value	Unit
V _{DSS}	Drain-to-Source Voltage		800	V
V_{GS}	Gate-to-Source Voltage	DC	±20	V
		AC (f > 1 Hz)	±30	
I _D	Drain Current	Continuous (T _C = 25°C)	13*	А
		Continuous (T _C = 100°C)	8.2*	
I _{DM}	Drain Current	Pulsed (Note 1)	32.5*	А
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		TBD	mJ
I _{AS}	Avalanche Current (Note 2)		TBD	А
E _{AR}	Repetitive Avalanche Energy (Note 1)		TBD	mJ
dv/dt	MOSFET dv/dt		100	V/ns
	Peak Diode Recovery dv/dt (Note 3)		20	
P_{D}	Power Dissipation	(T _C = 25°C)	152	W/°C
		Derate Above 25°C	TBD	1
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150	°C
TL	Lead Temperature Soldering Reflow for Soldering Purposes (1/8" from Case for 10 seconds)		260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected. *Drain current limited by maximum junction temperature, with heatsink.

1. Repetitive rating: pulse–width limited by maximum junction temperature.

2. $I_{AS} = TBD \ A, R_G = 25 \ \Omega$, starting $T_J = 25^{\circ}C$.

3. $I_{SD} \le 6 \ A$, di/dt $\le 200 \ A/\mu s$, $V_{DD} \le 400 \ V$, starting $T_J = 25^{\circ}C$.

THERMAL RESISTANCE RATINGS

Symbol	Parameter	Value	Unit
$R_{ heta JC}$	Junction-to-Case - Steady State	TBD	°C/W
$R_{\theta JA}$	Junction-to-Ambient - Steady State	TBD	

PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Marking	Package	Packing Method	Reel Size	Tape Width	Quantity
NTPF360N80S3Z	NTPF360N80S3Z	TO-220 FULLBACK	Tube	N/A	N/A	50 Units

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
OFF CHARACT	TERISTICS					
BV _{DSS}	Drain-to-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 1 \text{ mA}, T_J = 25^{\circ}\text{C}$	800			V
		V _{GS} = 0 V, I _D = 1 mA, T _J = 150°C	900			V
$\Delta BV_{DSS}/\Delta T_{J}$	Breakdown Voltage Temperature Coefficient	I _D = 1 mA, Referenced to 25°C		0.96		V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 800 V, V _{GS} = 0 V			10	μΑ
		V _{DS} = 640 V, T _C = 125°C		20		1
I _{GSS}	Gate-to-Body Leakage Current	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$			10	μΑ
ON CHARACTI	ERISTICS					
V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 0.3 \text{ mA}$	2.2		3.8	V
R _{DS(on)}	Static Drain-to-Source On Resistance	V _{GS} = 10 V, I _D = 6.5 A		300	360	mΩ
9FS	Forward Transconductance	V _{DS} = 20 V, I _D = 6.5 A		13.1		S
DYNAMIC CHA	RACTERISTICS					
C _{iss}	Input Capacitance	$V_{DS} = 400 \text{ V}, V_{GS} = 0 \text{ V}, f = 250 \text{ kHz}$		1120		pF
C _{oss}	Output Capacitance			16.4		pF
C _{oss(eff.)}	Effective Output Capacitance	V _{DS} = 0 V to 400 V, V _{GS} = 0 V		315		pF
C _{oss(er.)}	Energy Related Output Capacitance	V _{DS} = 0 V to 400 V, V _{GS} = 0 V		34		pF
Q _{g(tot)}	Total Gate Charge at 10 V	V _{DS} = 400 V, I _D = 6.5 A, V _{GS} = 10 V (Note 4)		24.7		nC
Q_{gs}	Gate-to-Source Gate Charge			6		nC
Q_{gd}	Gate-to-Drain "Miller" Charge			10.2		nC
ESR	Equivalent Series Resistance	f = 1 MHz		3.6		Ω
SWITCHING CI	HARACTERISTICS					
t _{d(on)}	Turn-On Delay Time	$V_{DD} = 400 \text{ V}, I_D = 6.5 \text{ A}, V_{GS} = 10 \text{ V},$		20.3		ns
t _r	Turn-On Rise Time	$R_g = 25 \Omega$ (Note 4)		2.8		ns
t _{d(off)}	Turn-Off Delay Time			37		ns
t _f	Turn-Off Fall Time			10.1		ns
SOURCE-DRA	IN DIODE CHARACTERISTICS				-	
I _S	Maximum Continuous Source-to-Drain Diode Forward Current				13	Α
I _{SM}	Maximum Pulsed Source-to-Drain Diode Forward Current				32.5	Α
V _{SD}	Source-to-Drain Diode Forward Voltage	$V_{GS} = 0 \text{ V}, I_{SD} = 6.5 \text{ A}$			1.2	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _{SD} = 6.5 A,		370		ns
Q _{rr}	Reverse Recovery Charge	dl _F /dt = 100 A/μs		3.2		μC

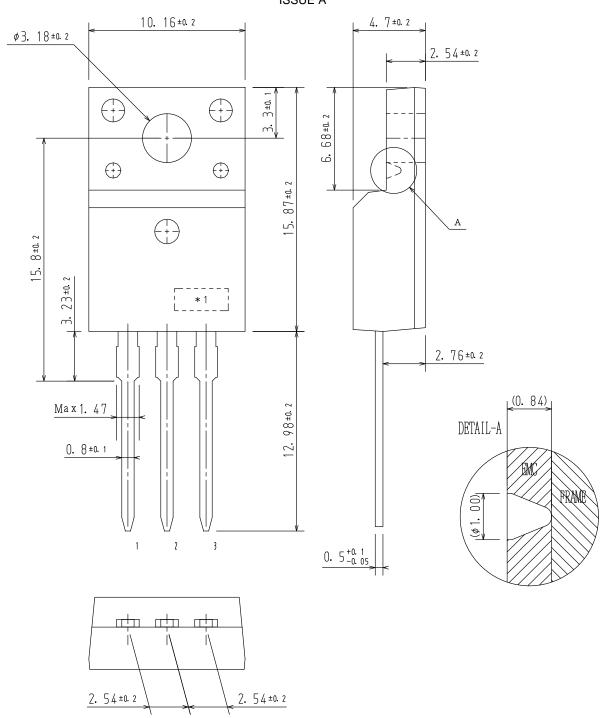
Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

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^{4.} Essentially independent of operating temperature typical characteristics.

PACKAGE DIMENSIONS

TO-220 Fullpack, 3-Lead / TO-220F-3SG CASE 221AT ISSUE A



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