Product Preview

Spread Spectrum EMI Reduction IC for HD Display

Description

PCS3P6200A is a versatile spread spectrum modulator designed specifically for a wide range of clock frequencies. The device addresses the need of a low EMI clock generator for use in display systems covering wide choice of pixel frequencies.

PCS3P6200A reduces electromagnetic interference (EMI) at the clock source, allowing system wide reduction of EMI of all clock dependent signals. PCS3P6200A allows significant system cost savings by reducing the number of circuit board layers, ferrite beads, shielding that are traditionally required to pass EMI regulations.

The Supply Voltage of the Device is 3.3 V/2.5 V. It has two Spread Selection Pins, SS1% and SS2% to select among the four possible deviation options. The Frequency Deviation across the Frequency range remains within $\pm 10\%$ of the selected deviation. Refer to the Frequency Deviation Selection Table for details. The Device is available in a 6 Pin TSOT-26 Package, over Commercial, Industrial and Extended Industrial temperature range.

Application

PCS3P6200A is targeted for use in a broad range of applications including Liquid Crystal and Plasma Displays.

Features

- Clock Generator for Display Systems
- Wide Operating Frequency Range Covering Most of the Pixel Frequencies
- Generates a Low EMI 1x Output
- Frequency Range: 25 MHz 120 MHz
- 4 Frequency Deviation Selection Options: ±1.50%, ±1.25%, ±0.75%, ±1.00%
- Supply Voltage: $3.3 \text{ V} \pm 0.3 \text{ V}$

 $2.5 \text{ V} \pm 0.125 \text{ V}$

- ModRate 85 KHz @ 72 MHz
- 6 Pin TSOT-26 package
- Commercial, Industrial and Extended Industrial Temperature Range
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

This document contains information on a product under development. ON Semiconductor reserves the right to change or discontinue this product without notice.



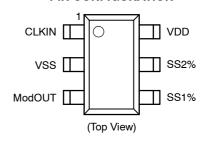
ON Semiconductor®

http://onsemi.com



TSOT-6 J SUFFIX CASE 419AF

PIN CONFIGURATION



ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 6 of this data sheet.

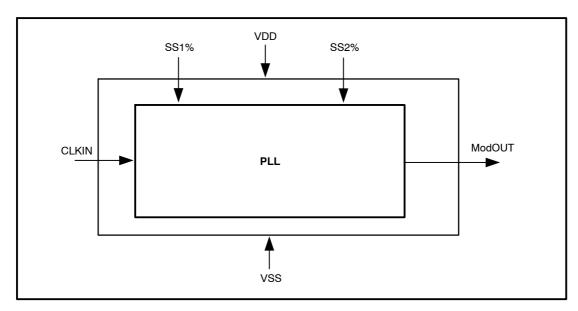


Figure 1. Block Diagram

Table 1. PIN DESCRIPTION

Pin#	Pin Name	Туре	Description
1	CLKIN	I	External Reference Clock Input.
2	VSS	Р	Ground to entire chip.
3	ModOUT	0	Modulated Frequency Output.
4	SS1%	I	Frequency Deviation Selection. Refer to <i>Frequency Deviation Selection Table</i> for details. Has an Internal pull-up resistor.
5	SS2%	I	Frequency Deviation Selection. Refer to <i>Frequency Deviation Selection Table</i> for details. Has an Internal pull-up resistor.
6	VDD	Р	Power to entire chip.

Table 2. FREQUENCY DEVIATION SELECTION TABLE

SS2%	SS1%	Frequency Deviation
L	L	±1.50%
L	Н	±1.25%
Н	L	±0.75%
Н	Н	±1.00%

Table 3. ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Rating	Unit
V_{DD}, V_{IN}	Voltage on any pin with respect to Ground	-0.5 to +4.6	V
T _{STG}	Storage temperature	-65 to +125	°C
T _s	Max. Soldering Temperature (10 sec)	260	°C
T_J	Junction Temperature	150	°C
T _{DV}	Static Discharge Voltage (As per JEDEC STD22- A114-B)	2	KV

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Table 4. OPERATING CONDITIONS FOR 2.5 V AND 3.3 V SUPPLY VOLTAGE

Parameter	D	Min	Max	Unit	
V _{DD(2.5)}	Supply Voltage		2.375	2.625	V
V _{DD(3.3)}				3.6	1
T _A	Operating Temperature	Commercial	0	+70	°C
		Industrial	-40	+85	
		Extended Industrial	-40	+105	
C _L	Load Capacitance	•		15	pF

Table 5. DC ELECTRICAL CHARACTERISTICS FOR 2.5 V SUPPLY

Symbol	Parameter	Min	Тур	Max	Unit
V _{IL}	Input low voltage	VSS - 0.3		0.7	V
V _{IH}	Input high voltage	1.7		VDD + 0.3	V
I _{IL}	Input low current			-35	μΑ
I _{IH}	Input high current			35	μΑ
V _{OL}	Output low voltage (VDD = 2.5 V, I _{OL} = 8 mA)			0.6	V
V _{OH}	Output high voltage (VDD = 2.5 V, I _{OH} = -8 mA)	1.8			٧
I _{DD}	Static supply current (Note 1)			4	mA
I _{CC}	Dynamic supply current, Unloaded Output		9	11	mA
V_{DD}	Operating voltage	2.375	2.5	2.625	V
t _{ON}	Power-up time (first locked cycle after power-up)			3	mS
C _{IN}	Input Capacitance		5		pF
Z _{OUT}	Output Impedance		40		Ω

^{1.} CLKIN pin is pulled low.

Table 6. AC ELECTRICAL CHARACTERISTICS FOR 2.5 V SUPPLY

Symbol	Parameter	Min	Тур	Max	Unit
CLKIN	Input frequency	25		120	MHz
ModOUT	Output frequency	25		120	MHz
t _{LH} (Note 2)	Output rise time (measured from 0.7 V to 1.7 V)		2	2.7	nS
t _{HL} (Note 2)	(Note 2) Output fall time (measured from 1.7 V to 0.7 V)		1	1.5	nS
t _{JC}	t _{JC} Jitter (Cycle-to-cycle)		±250	±300	pS
t _D	Output duty cycle	40	50	60	%

^{2.} t_{LH} and t_{HL} are measured into a capacitive load of 15 pF.

Table 7. DC ELECTRICAL CHARACTERISTICS FOR 3.3 V SUPPLY

Symbol	Parameter	Min	Тур	Max	Unit
V _{IL}	Input low voltage	VSS - 0.3		0.8	V
V _{IH}	Input high voltage	2.0		VDD + 0.3	V
I _{IL}	Input low current			-35	μΑ
I _{IH}	Input high current			35	μΑ
V _{OL}	Output low voltage (VDD = 3.3 V, I _{OL} = 8 mA)			0.4	V
V _{OH}	Output high voltage (VDD = 3.3 V, I _{OH} = -8 mA)	2.5			V
I _{DD}	Static supply current (Note 3)			4.5	mA
I _{CC}	Dynamic supply current, Unloaded Output		11	14	mA
V_{DD}	Operating voltage	3.0	3.3	3.6	V
t _{ON}	Power-up time (first locked cycle after power-up)			3	mS
C _{IN}	Input Capacitance		5		pF
Z _{OUT}	Output Impedance		35		Ω

^{3.} CLKIN pin is pulled low.

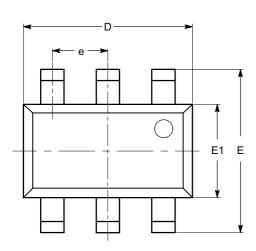
Table 8. AC ELECTRICAL CHARACTERISTICS FOR 3.3 V SUPPLY (Note 5)

Symbol	Parameter	Min	Тур	Max	Unit
CLKIN	Input frequency	25		120	MHz
ModOUT	Output frequency	25		120	MHz
t _{LH} (Note 4)	Output rise time (measured from 0.8 V to 2.0 V)		1.3	2	nS
t _{HL} (Note 4)	Output fall time (measured at 2.0 V to 0.8 V)		0.9	1.3	nS
tuc	Jitter (Cycle-to-cycle)		±225	±300	pS
t _D	Output duty cycle	45	50	55	%

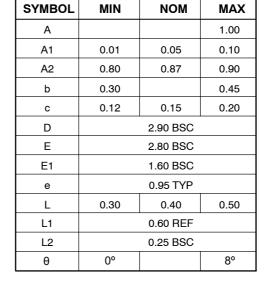
t_{LH} and t_{HL} are measured into a capacitive load of 15 pF.
All parameters are at an Extended Industrial temperature range unless otherwise stated.

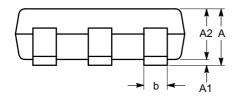
PACKAGE DIMENSIONS

TSOT-23, 6 LEAD CASE 419AF-01 ISSUE O

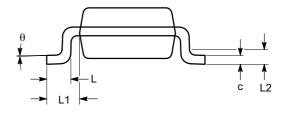


TOP	VIEW
-----	------





SIDE VIEW



END VIEW

Notes:

- (1) All dimensions are in millimeters. Angles in degrees.
- (2) Complies with JEDEC MO-193.

Table 9. ORDERING INFORMATION

Part Number	Marking*	Package Type	Temperature
PCS3P6200AG-06JR	AC4LL	6-Pin TSOT-26, TAPE & REEL, Green	Commercial
PCS3I6200AG-06JR	AC2LL	6-Pin TSOT-26, TAPE & REEL, Green	Industrial
PCS3E6200AG-06JR	AC5LL	6-Pin TSOT-26, TAPE & REEL, Green	Extended Industrial

^{*} LL = 2 Character LOT #

ON Semiconductor and are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800–282–9855 Toll Free USA/Canada

Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910 Japan Customer Focus Center Phone: 81-3-5773-3850 ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative