

## Description

The SST39SF010A / SST39SF020A / SST39SF040 are CMOS Multi-Purpose Flash (MPF) devices manufactured with SST proprietary, high performance CMOS SuperFlash technology. The split-gate cell design and thick oxide tunneling injector attain better reliability and manufacturability compared with alternate approaches. The SST39SF010A / SST39SF020A / SST39SF040 write (Program or Erase) with a 4.5-5.5V power supply, and conforms to JEDEC standard pinouts for x8 memories.

This document provides supplemental information about the 45 ns SST39SF010A / SST39SF020A / SST39SF040 parts which are End-of-Life (EOL). Except for the information provided herein, the 45 ns parts behave as described in the SST39SF010A / SST39SF020A / SST39SF040 data sheet DS-25002. See page 3 for specific part numbers.

## AC Characteristics

**Table 1: AC Conditions of Test**

| Input Rise/Fall Time | Output Load                     |
|----------------------|---------------------------------|
| 5ns                  | $C_L = 30 \text{ pF}$ for 45 ns |

T1.1 25162

**Table 2: Read Cycle Timing Parameters  $V_{DD} = 4.5\text{-}5.5\text{V}$** 

| Symbol      | Parameter                       | SST39SF010A/020A/040-45 |     | Units |
|-------------|---------------------------------|-------------------------|-----|-------|
|             |                                 | Min                     | Max |       |
| $T_{RC}$    | Read Cycle Time                 | 45                      |     | ns    |
| $T_{CE}$    | Chip Enable Access Time         |                         | 45  | ns    |
| $T_{AA}$    | Address Access Time             |                         | 45  | ns    |
| $T_{OE}$    | Output Enable Access Time       |                         | 30  | ns    |
| $T_{CLZ}^1$ | CE# Low to Active Output        | 0                       |     | ns    |
| $T_{OLZ}^1$ | OE# Low to Active Output        | 0                       |     | ns    |
| $T_{CHZ}^1$ | CE# High to High-Z Output       |                         | 15  | ns    |
| $T_{OHZ}^1$ | OE# High to High-Z Output       |                         | 15  | ns    |
| $T_{OH}^1$  | Output Hold from Address Change | 0                       |     | ns    |

T2.4 25162

1. This parameter is measured only for initial qualification and after a design or process change that could affect this parameter.

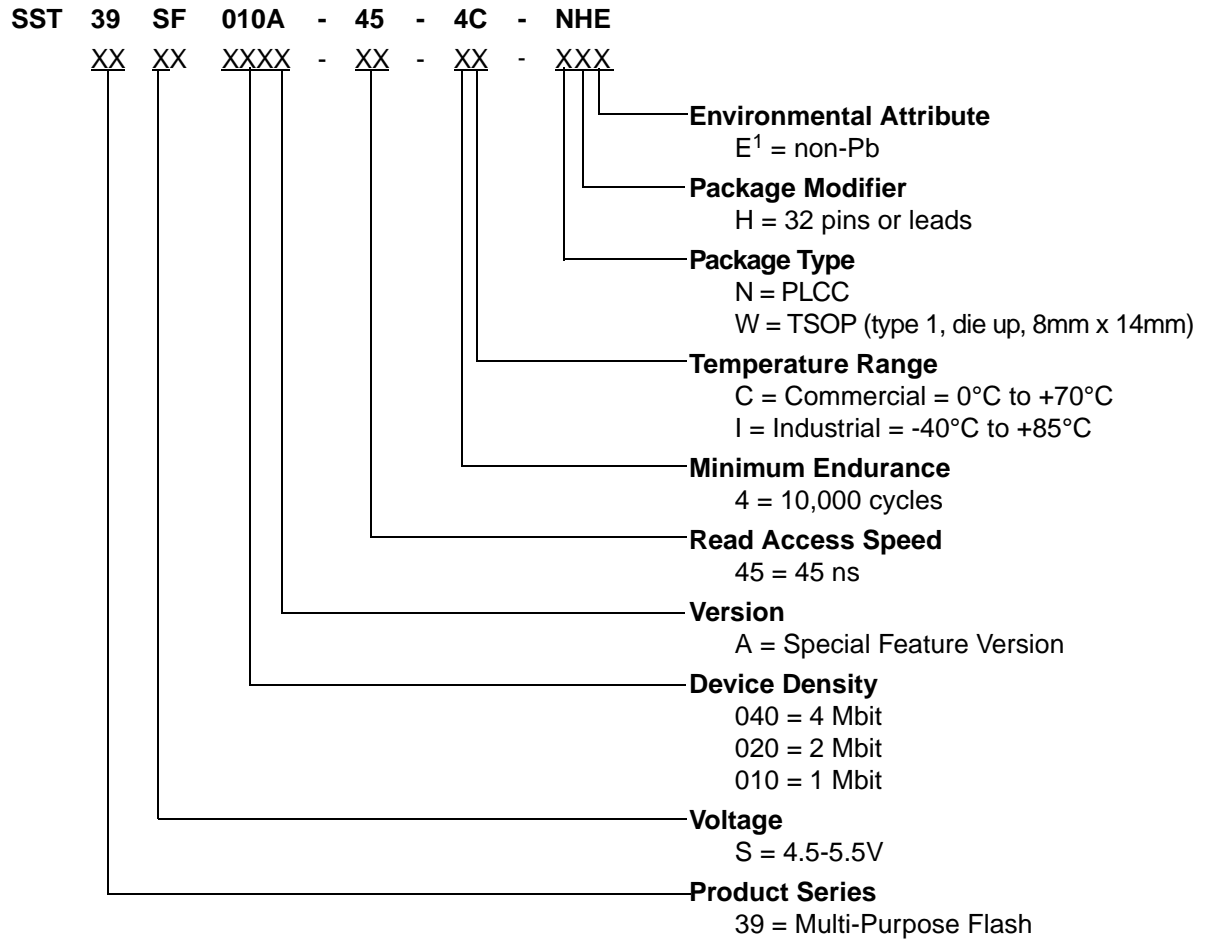


# SST39SF010A / SST39SF020A / SST39SF040

## 45 ns EOL Supplemental Information

EOL Data Sheet

### Product Ordering Information



1. Environmental suffix "E" denotes non-Pb solder. SST non-Pb solder devices are "RoHS Compliant".



# SST39SF010A / SST39SF020A / SST39SF040

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### Valid combinations for SST39SF010A

|                       |                       |
|-----------------------|-----------------------|
| SST39SF010A-45-4C-NHE | SST39SF010A-45-4C-WHE |
| SST39SF010A-45-4I-NHE | SST39SF010A-45-4I-WHE |

### Valid combinations for SST39SF020A

|                       |                       |
|-----------------------|-----------------------|
| SST39SF020A-45-4C-NHE | SST39SF020A-45-4C-WHE |
| SST39SF020A-45-4I-NHE | SST39SF020A-45-5I-WHE |

### Valid combinations for SST39SF040

|                      |                      |
|----------------------|----------------------|
| SST39SF040-45-4C-NHE | SST39SF040-45-4C-WHE |
| SST39SF040-45-4I-NHE | SST39SF040-45-4I-WHE |

**Note:** Valid combinations are those products in mass production or will be in mass production. Consult your SST sales representative to confirm availability of valid combinations and to determine availability of new combinations.



# SST39SF010A / SST39SF020A / SST39SF040

## 45 ns EOL Supplemental Information

EOL Data Sheet

**Table 3:** Revision History

| Revision | Description  | Date     |
|----------|--|----------|
| A        | <ul style="list-style-type: none"><li>EOL document for 45 ns parts</li></ul> | Mar 2013 |

ISBN:978-1-62077-168-6

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Specifications are subject to change without notice. Refer to [www.microchip.com](http://www.microchip.com) for the most recent documentation. For the most current package drawings, please see the Packaging Specification located at <http://www.microchip.com/packaging>.

Memory sizes denote raw storage capacity; actual usable capacity may be less.

SST makes no warranty for the use of its products other than those expressly contained in the Standard Terms and Conditions of Sale.

For sales office locations and information, please see [www.microchip.com](http://www.microchip.com).

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