

# Bluetooth® 4.2 Stereo Audio SoC

#### **Features**

- · Qualified for Bluetooth v4.2
- HFP 1.6, HSP 1.2, A2DP 1.3, SPP 1.2, and AVRCP 1.6
- · Supports Bluetooth Low Energy (BLE):
  - Generic Access Service
  - Device Information Service
  - Proprietary Services for data communication
  - Apple Service Notification Center (ANCS)
- · 24-bit, 96 kHz high resolution audio
- MIC, analog audio, I<sup>2</sup>S digital audio, AUX-in
- · 1 MIC input supported
- · UART, GPIOs, LEDs
- · Firmware upgrade
- · Battery charging

#### **Baseband Features**

- · 16 MHz main clock input
- · Built-in Flash memory for programing
- Connects two hosts (smart phones and tablets) with HFP or A2DP profiles, simultaneously
- Adaptive Frequency Hopping (AFH)

# **RF Features**

- Class 2 output power (+2 dBm typical)
- · Receive sensitivity: -90 dBm (2 Mbps EDR)
- Combined Tx/Rx RF terminal simplifies external matching and reduces external antenna switches
- · Tx/Rx RF switch for Class 2 or Class 3 application
- Integrated synthesizer requires no external voltage-controlled oscillator (VCO), varactor diode, resonator or loop filter
- Crystal oscillator with built-in digital trimming for temperature or process variations

#### **Audio Codec**

- Sub-band coding (SBC) and optional Advanced Audio Coding (AAC)
- · 20-bit digital-to-analog (DAC) with 98 dB SNR
- · 16-bit analog-to-digital (ADC) with 92 dB SNR
- Up to 24-bit or 96 kHz on I<sup>2</sup>S digital audio

# **DSP Audio Processing**

- · 32-bit DSP core
- 64 kb/s A-Law, μ-Law PCM format, or Continuous Variable Slope Delta Modulation (CVSD) for SCO channel operation
- · 8 kHz or 16 kHz noise suppression
- 8 kHz or 16 kHz echo cancellation
- Packet loss concealment (PLC)
- Serial Copy Management System (SCMS-T) content protection

### **Peripherals**

- High-speed HCI-UART interface (supports up to 921,600 bps)
- Lithium-ion and lithium-polymer battery charger (up to 350 mA)
- Integrated 3V and 1.8V configurable switching regulator and low-dropout (LDO)
- · ADC for battery monitor and voltage sensor
- AUX-in port for external audio input
- · LED drivers

# **Operating Condition**

Operating voltage: 3.2V to 4.2V

Operating temperature: -20°C to +70°C

#### **Package**

- 8 mm x 8 mm, 68-LGA package
- 0.4 mm pitch

# **Applications**

- · Sound bars and subwoofers
- · Speaker phones
- · Headsets and headphones

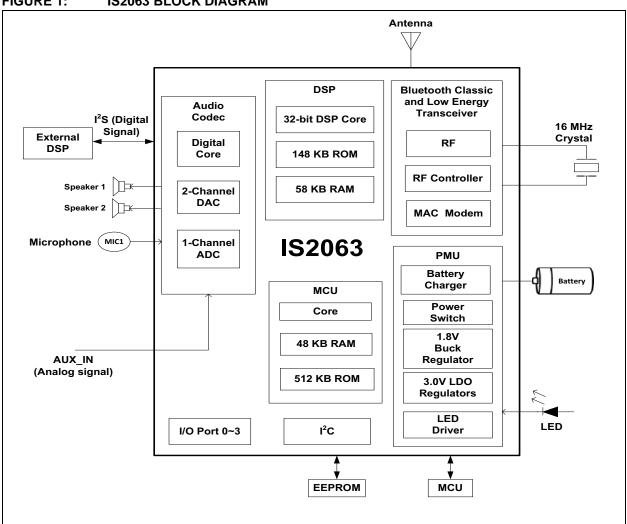
## **Description**

The IS2063 is a SoC qualified for Bluetooth v4.2 with Enhanced Data Rate (EDR). It integrates a 32-bit DSP co-processor and a codec dedicated for voice and audio applications. The Continuously Variable Slope Delta (CVSD) encoding or decoding, 8K/16K noise reduction and echo cancellation are implemented for voice applications. The Sub-band coding (SBC) and AAC Low-Complexity (AAC-LC) decoding functions are used for audio applications.

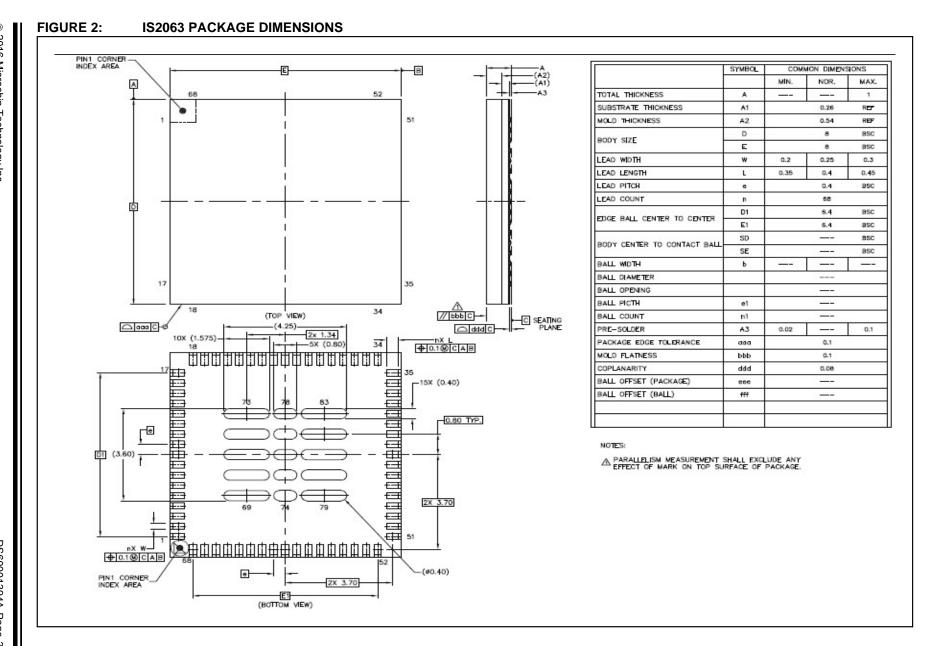
The IS2063 SoC features a 20-bit audio DAC in additional to an I<sup>2</sup>S digital audio interface that supports 24-bit or 96 kHz data formats. System optimizations include an integrated battery voltage sensor, battery charger, switching regulator, and LDOs.

Figure 1 illustrates a typical block diagram of the IS2063 SoC. Figure 2 illustrates the IS2063 package dimensions.

FIGURE 1: IS2063 BLOCK DIAGRAM







# **IS2063**

**NOTES:** 

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