



## Fan Control Device with Hardware Monitoring and Acoustic Noise Reduction Features

### PRODUCT FEATURES

Data Brief

- 3.3 Volt Operation (5 Volt Tolerant Input Buffers)
- SMBus 2.0 compliant interface (Fixed, Not Discoverable) With Three Slave Address Options
- Fan Control
  - PWM (Pulse width Modulation) Outputs (3)
  - Fan Tachometer Inputs (4)
  - Individual status and enable bits per tach input
  - Programmable automatic fan control based on temperature
  - Fan ramp rate control for acoustic noise reduction
- Temperature Monitor
  - Monitoring of Two Remote Thermal Diodes (+/- 3 deg C accuracy)
  - Internal Ambient Temperature Measurement
  - Limit Comparison of all Monitored Values
  - Individual status and enable bits per thermal input
  - Interrupt Pin for out-of-limit Temperature Indication
  - Configurable offset for internal or external temperature channels.
  - Supports 4 programmable temperature averaging modes
  - 2 monitoring modes: Continuous or Cycle (Power Saving mode)
  - Offers 2 Low Power Modes when monitoring is off: Sleep and Shutdown
- Voltage Monitor
  - Monitor Power supplies (+2.5V, +5V, +12V, Vccp, and VCC)
  - Individual status and enable bits per voltage input
  - Limit Comparison of all Monitored Values
  - Interrupt Pin for out-of-limit Voltage Indication
  - Supports 4 programmable voltage averaging modes
  - 2 monitoring modes: Continuous or Cycle (Power Saving mode)
  - Offers 2 Low Power Modes when monitoring is off: Sleep and Shutdown
- 5 VID (Voltage Identification) inputs
- XOR Tree test mode
- 24-pin, SSOP Lead-Free RoHS Compliant package

**ORDER NUMBERS:****EMC6D102-CZC FOR 24 PIN, SSOP LEAD-FREE ROHS COMPLIANT PACKAGE****EMC6D102-CZC-TR FOR 24 PIN, SSOP LEAD-FREE ROHS COMPLIANT PACKAGE**

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## General Description

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The EMC6D102 is an environmental monitoring device with automatic fan control capability. This ACPI compliant device provides hardware monitoring for up to five voltages and three thermal zones, measures the speed of up to four fans, and controls the speed of multiple DC fans using Pulse Width Modulator (PWM) outputs.

The EMC6D102 hardware monitor provides analog inputs for monitoring external voltages of +2.5V, +5V, +12V and V<sub>ccp</sub>. This device has the capability to monitor its own internal VCC power supply, which may be connected to either main power (VCC) or the suspend power well (VTR). In addition to monitoring the processor voltage, VID inputs are available to identify the voltage specification. External components are not required for voltage scaling or similar treatment.

The EMC6D102 hardware monitor includes support for monitoring three thermal zones: two external and one internal. The external temperatures are measured via thermal diode inputs capable of monitoring remote devices. In addition, they are equipped with an ambient temperature sensor for measuring the internal temperature.

Pulse Width Modulators (PWM) control the speed of the fans by varying the output duty cycle of the PWM. Each PWM can be associated with any or all of the thermal zones monitored. As the temperature of the associated zone varies, the PWM duty cycle is adjusted accordingly. The Ramp Rate Control feature controls the rate of change of the PWM output, thereby reducing system noise created by changing the fan speed. The speed of each fan is monitored by a Fan Tachometer input. The measured values are compared to values stored in Limit Registers to detect if a fan has stalled or seized.

Fan speed may be under host software control or automatic. In host control mode, the host software continuously monitors temperature and fan speed registers, makes decisions as to desired fan speed and sets the PWM's to drive the required fan speed. This device offers an interrupt output signal (INT#), which may be used to interrupt the host on out-of-limit temperature or voltage condition enabling an ACPI response as opposed to the host software continuously monitoring status. In auto "zone" mode, the logic continuously monitors the temperature and fan speeds and adjusts speeds without intervention from the host CPU. Fan speed is adjusted according to an algorithm using the temperature measured in the selected zone, the high and low limits set by the user, and the current fan speed.

## Pin Layout Diagram

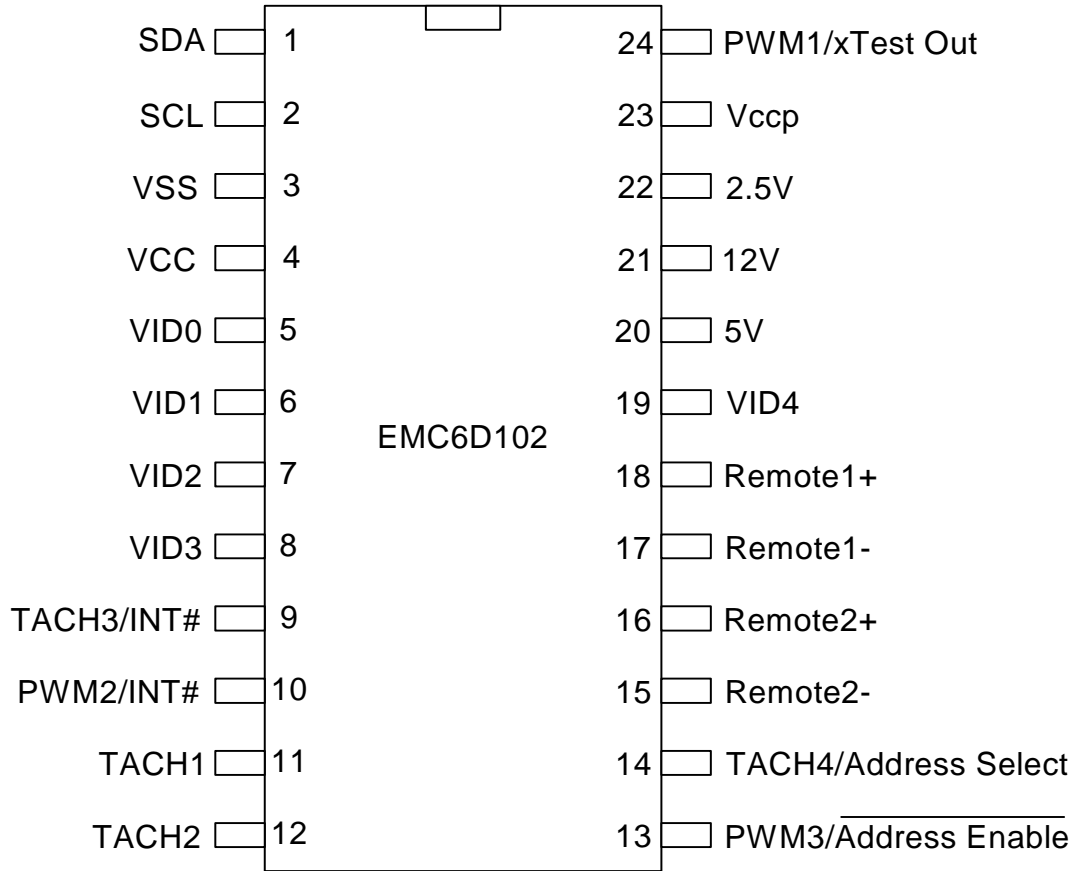


Figure 1 EMC6D102 Pin Diagram

# Package Outline

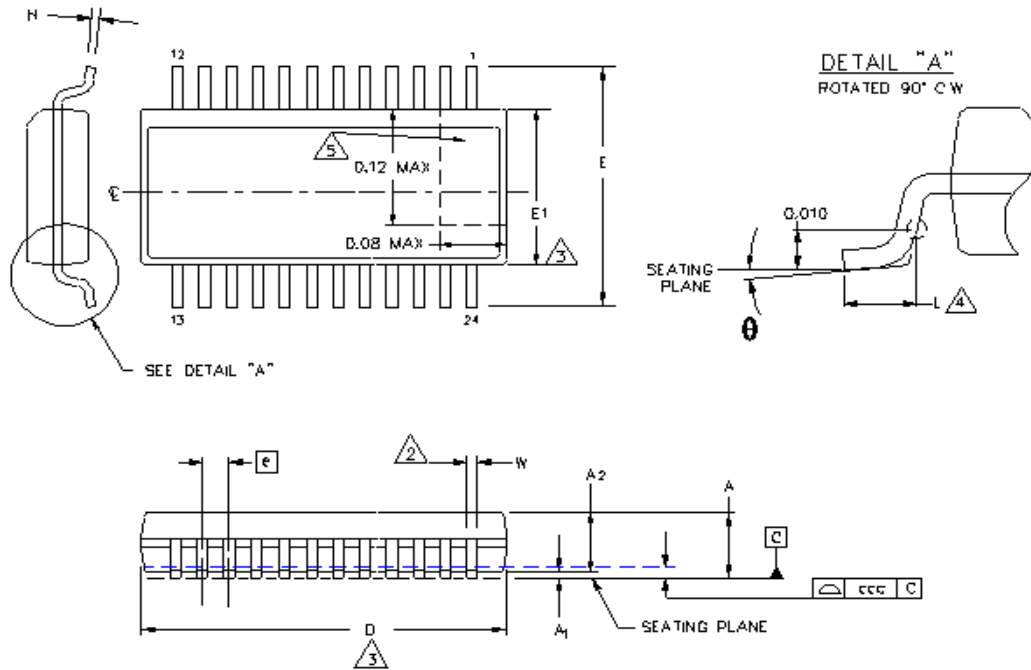


Figure 2 24-Pin SSOP Package Outline, 0.150” Wide Body, 0.025” Pitch

Table 1 24-pin SSOP Package Parameters

	MIN	NOMINAL	MAX	REMARKS
<b>A</b>	0.053	~	0.069	Overall Package Height
<b>A1</b>	0.004	~	0.010	Standoff
<b>A2</b>	~	~	0.061	Body Thickness
<b>D</b>	0.337	~	0.344	X Body Size
<b>E</b>	0.228	~	0.244	Y Span
<b>E1</b>	0.150	~	0.157	Y body Size
<b>H</b>	0.007	~	0.010	Lead Frame Thickness
<b>L</b>	0.016	0.025	0.050	Lead Foot Length
<b>e</b>	0.025 Basic			Lead Pitch
<b>è</b>	0°	~	8°	Lead Foot Angle
<b>W</b>	0.008	0.010	0.012	Lead Width
<b>ccc</b>	~	~	0.004	Coplanarity

**Notes:**

1. Controlling Unit: inch.
2. Tolerance on the true position of the leads is ± 0.0035 inches maximum.
3. Package body dimensions D and E1 do not include the mold protrusion. Maximum mold protrusion is 0.006 inches for ends, and 0.010 inches for sides.
4. Dimension for foot length L measured at the gauge plane 0.010 inches above the seating plane.
5. Details of pin 1 identifier are optional but must be located within the zone indicated.