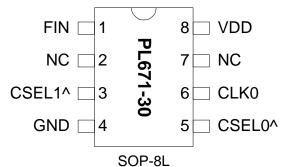


#### **FEATURES**

- Accepts Reference Clock input
  - o Reference Clock: 1MHz 200MHz
- Output Frequency up to 200MHz
- Center and Down Spread Spectrum Modulation
- Four selectable pre-programmed configurations
- Max 100ps Cycle to Cycle jitter
- Single 2.5V ~ 3.3V, ± 10% power supply
- Operating temperature range from -40°C to 85°C
- Available in SOP-8L GREEN/RoHS compliant Package

### **PIN CONFIGURATION**



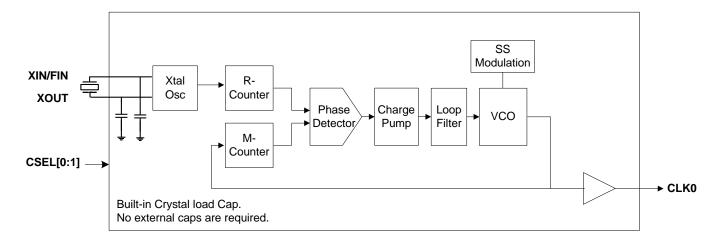
Note: ^ Denotes 60KΩ Pull-up resistor

### **DESCRIPTION**

The PL671-30 is a high performance programmable spread spectrum clock. It allows for programming the modulation type (Center or Down Spread) and 16 modulation magnitudes (±0.125% to ±2.0% or -0.25% to -4.0%). The CSEL[0:1] pins can switch between four pre-programmed configurations adding to the flexibility.

| CSEL1 | CSEL0 | Input<br>(MHz) | Spread<br>Spectrum Type           | Spread Spectrum<br>Modulation Amplitude                              | Output<br>(MHz)                         |
|-------|-------|----------------|-----------------------------------|--|---|
| 0     | 0     |                |                                   |  |   |
| 0     | 1     | 1 200          | Configurable as<br>Center or Down | Configurable as<br>Center: ±0.125% to ±2.0%<br>Down: -0.25% to -4.0% | 1 – 200<br>(Programmable<br>Multiplier) |
| 1     | 0     | 1 - 200        |                                   |  |   |
| 1     | 1     |                |                                   |  |   |

#### **BLOCK DIAGRAM**





#### PACKAGE PIN ASSIGNMENT

| Name      | SOP-8L | Туре | Description  |
|-----------|--------|------|--|
| FIN       | 1      | I    | Reference clock input pin  |
| NC        | 2,7    | -    | No Connection  |
| CSEL[1:0] | 3,5    | I    | Selector pins used to toggle between four pre-programmed configurations. These pins have 60K ohm internal pull up resistor. There are four possible pre-defined configurations can be chosen from the following combination.  - Spread Spectrum modulation Type (Center or Down)  - Spread Spectrum modulation Amplitude  Center Spread - ±0.125% to ±2.0% in ±0.125% steps  Down Spread0.25% to -4.0% in 0.25% steps)  - Output Clock Drive Strength (4mA, 8mA, 16mA) |
| GND       | 4      | Р    | Ground connection  |
| CLK0      | 6      | 0    | Spread Spectrum Clock Output   |
| VDD       | 8      | Р    | VDD connection (2.25~3.63V)  |

### LAYOUT RECOMMENDATIONS

The following guidelines are to assist you with a performance optimized PCB design:

# Signal Integrity and Termination Considerations

- Keep traces as short as possible.
- Trace = Inductor. With a capacitive load this equals ringing!
- Long trace = Transmission Line. Without proper termination this will cause reflections (looks like ringing).
- Design long traces (>1 inch) as "striplines" or "microstrips" with defined impedance.
- Match trace at one side to avoid reflections bouncing back and forth.

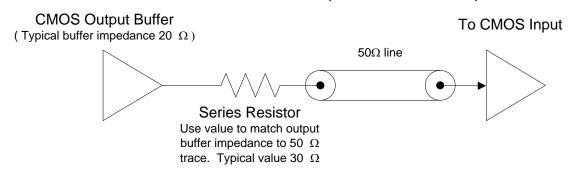
## Decoupling and Power Supply Considerations

- Place decoupling capacitors as close as possible to the VDD pin(s) to limit noise from the power supply
- Multiple VDD pins should be decoupled separately for best performance.
- Addition of a ferrite bead in series with VDD can help prevent noise from other board sources
- Value of decoupling capacitor is frequency dependant. Typical values to use are  $0.1\mu F$  for designs using frequencies < 50 MHz and  $0.01\mu F$  for designs using frequencies > 50 MHz.



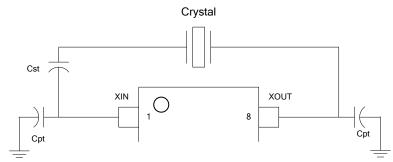
### **Typical CMOS termination**

Place Series Resistor as close as possible to CMOS output



#### **Crystal Tuning Circuit**

Series and parallel capacitors used to fine tune the crystal load to the circuit load.



**CST** - Series Capacitor, used to lower circuit load to match crystal load. Raises frequency offset. This can be eliminated by using a crystal with a Cload of equal or greater value than the oscillator.

**CPT** - Parallel Capacitors, Used to raise the circuit load to match the crystal load. Lowers frequency offset.



# ELECTRICAL SPECIFICATIONS ABSOLUTE MAXIMUM RATINGS

| PARAMETERS                            | SYMBOL   | MIN. | MAX.                 | UNITS |
|---------------------------------------|----------|------|----------------------|-------|
| Supply Voltage Range                  | $V_{DD}$ | -0.5 | 4.6                  | V     |
| Input Voltage Range                   | Vı       | -0.5 | V <sub>DD</sub> +0.5 | V     |
| Output Voltage Range                  | Vo       | -0.5 | V <sub>DD</sub> +0.5 | V     |
| Soldering Temperature (Green package) |          |      | 260                  | °C    |
| Data Retention @ 85°C                 |          | 10   |                      | Year  |
| Storage Temperature                   | Ts       | -65  | 150                  | °C    |
| Ambient Operating Temperature*        |          | -40  | 85                   | °C    |

Exposure of the device under conditions beyond the limits specified by Maximum Ratings for extended periods may cause permanent damage to the device and affect product reliability. These conditions represent a stress rating only, and functional operations of the device at these or any other conditions above the operational limits noted in this specification is not implied. \*Operating temperature is guaranteed by design. Parts are tested to commercial grade only.

### **AC SPECIFICATIONS**

| PARAMETERS                   | CONDITIONS   | MIN. | TYP. | MAX.            | UNITS |
|------------------------------|--|------|------|-----------------|-------|
| Input /FIN) Fraguency        | @ V <sub>DD</sub> =3.3V  | 1    |      | 200             | MHz   |
| Input (FIN) Frequency        | @ V <sub>DD</sub> =2.5V  | 1    |      | 166             |       |
| Input (FIN) Signal Amplitude | Internally AC coupled (High Frequency)                               | 0.9  |      | $V_{DD}$        | Vpp   |
| Input (FIN) Signal Amplitude | Internally AC coupled (Low Frequency) 3.3V <50MHz, 2.5V <40MHz       | 0.1  |      | V <sub>DD</sub> | Vpp   |
| Output Fraguency             | @ V <sub>DD</sub> =3.3V  |      |      | 200             | MHz   |
| Output Frequency             | @ V <sub>DD</sub> =2.5V  |      |      | 166             | MHz   |
| Settling Time                | At power-up (after V <sub>DD</sub> increases over 2.25V)             |      |      | 2               | ms    |
| Output Bigg Time             | 15pF Load, 10/90% V <sub>DD</sub> , Standard Drive                   |      | 2.0  | 3.0             | ns    |
| Output Rise Time             | 15pF Load, 10/90% V <sub>DD</sub> , High Drive                       |      | 1.2  | 1.7             | ns    |
| Output Fall Time             | 15pF Load, 90/10% V <sub>DD</sub> , Standard Drive                   |      | 2.0  | 3.0             | ns    |
| Output Fall Time             | 15pF Load, 90/10% V <sub>DD</sub> , High Drive                       |      | 1.2  | 1.7             | ns    |
| Duty Cycle                   | At V <sub>DD</sub> /2  | 45   | 50   | 55              | %     |
| Cycle to Cycle Jitter*       | T <sub>CYC</sub> - <sub>CYC</sub> Over output frequency range @ 3.3V |      |      | 100             | ps    |

<sup>\*</sup> Note: Jitter performance depends on the programming parameters.



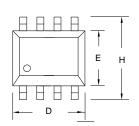
### DC SPECIFICATIONS

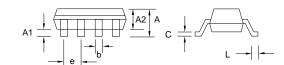
| PARAMETERS                                   | SYMBOL           | CONDITIONS                          | MIN.           | TYP. | MAX. | UNITS |
|--|------------------|-------------------------------------|----------------|------|------|-------|
| Supply Current, Dynamic, with Loaded Outputs | I <sub>DD</sub>  | At 25MHz, 3.3V,<br>load=15pF        |                |      | 15   | mA    |
| Operating Voltage                            | $V_{DD}$         |                                     | 2.25           |      | 3.63 | V     |
| Output Low Voltage                           | V <sub>OL</sub>  | I <sub>OL</sub> = +4mA (Std. Drive) |                |      | 0.4  | V     |
| Output High Voltage                          | V <sub>OH</sub>  | I <sub>OH</sub> = -4mA (Std. Drive) | $V_{DD} - 0.4$ |      |      | V     |
| Output Current, Low Drive                    | I <sub>OSD</sub> | $V_{OL} = 0.4V, V_{OH} = 2.4V$      | 4              |      |      | mA    |
| Output Current, Standard Drive               | Iosd             | $V_{OL} = 0.4V, V_{OH} = 2.4V$      | 8              |      |      | mA    |
| Output Current, High Drive                   | I <sub>ОНD</sub> | $V_{OL} = 0.4V, V_{OH} = 2.4V$      | 16             |      |      | mA    |

### PACKAGE DRAWING (GREEN PACKAGE COMPLIANT)

### SOP-8L

| Symbol   | Dimension in MM |      |  |  |
|----------|-----------------|------|--|--|
| Syllibol | Min.            | Max. |  |  |
| Α        | 1.35            | 1.75 |  |  |
| A1       | 0.10            | 0.25 |  |  |
| A2       | 1.25            | 1.50 |  |  |
| В        | 0.33            | 0.53 |  |  |
| С        | 0.19            | 0.27 |  |  |
| D        | 4.80            | 5.00 |  |  |
| E        | 3.80            | 4.00 |  |  |
| Н        | 5.80            | 6.20 |  |  |
| L        | 0.40            | 0.89 |  |  |
| Α        | 1.27 BSC        |      |  |  |







### ORDERING INFORMATION (GREEN PACKAGE COMPLIANT)

### For part ordering, please contact our Sales Department:

2180 Fortune Drive, San Jose, CA 95131, USA Tel: (408) 944-0800 Fax: (408) 474-1000

#### **PART NUMBER**

The order number for this device is a combination of the following: Part Number, Package Type and Operating Temperature Range

| <u>PL671-30-XXX</u>                               | <u>X</u> X-R |   |
|---|--------------|---|
| PART NUMBER —                                     |              | NONE= TUBE  |
| 3 DIGIT ID Code * ————                            |              | R=TAPE and REEL   |
| PACKAGE TYPE ———————————————————————————————————— |              | TEMPERATURE C=COMMERCIAL (0°C to 70°C) I = INDUSTRIAL (-40°C to 85°C) |

\* Micrel will assign a unique 3-digit ID code for each approved programmed part number.

| Part/Order Number | Marking <sup>†</sup> | Package Option            |
|-------------------|----------------------|---------------------------|
| PL671-30-XXXSC    | P671-30<br>XXX       | 8-Pin SOP (Tube)          |
| PL671-30-XXXSC-R  | P671-30<br>XXX       | 8-Pin SOP (Tape and Reel) |

† Note: 'XXX' designates marking identifier that, at times, could be independent of the part number.

Please consult your Micrel sales representative for marking information.

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